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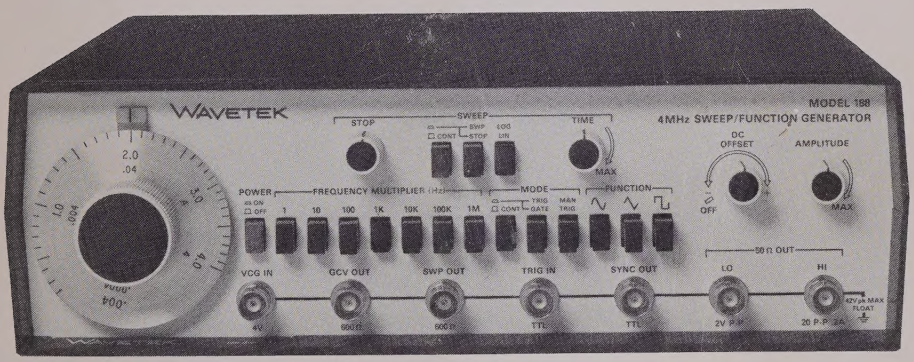
MODEL 188
4 MHz SWEEP/
FUNCTION GENERATOR



WAVETEK

INSTRUCTION MANUAL

MODEL 188
4 MHz SWEEP/ FUNCTION GENERATOR

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Instrument Release: D

WARRANTY

All Wavetek instruments are warranteed against defects in material and workmanship for a period of one year after date of manufacture. Wavetek agrees to repair or replace any assembly or component (except batteries) found to be defective, under normal use, during this period. Wavetek's obligation under this warranty is limited solely to repairing any such instrument which in Wavetek's sole opinion proves to be defective within the scope of the warranty when returned to the factory or to an authorized service center. Transportation to the factory or service center is to be prepaid by purchaser. Shipment should not be made without prior authorization by Wavetek.

This warranty does not apply to any products repaired or altered by persons not authorized by Wavetek, or not in accordance with instructions furnished by Wavetek. If the instrument is defective as a result of misuse, improper repair, or abnormal conditions or operations, repairs will be billed at cost.

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Any recommendations made by Wavetek for use of its products are based upon tests believed to be reliable, but Wavetek makes no warranty of the results to be obtained. This warranty is in lieu of all other warranties, expressed or implied, and no representative or person is authorized to represent or assume for Wavetek any liability in connection with the sale of our products other than set forth herein.

SAFETY

This instrument is wired for earth grounding via the facility power wiring. Do no bypass earth grounding with two wire extension cords, plug adapters, etc.

BEFORE PLUGGING IN the instrument, comply with installation instructions.

MAINTENANCE may require power on with the instrument covers removed. This should be done only by qualified personnel aware of the electrical hazards.

The instrument power receptical is connected to the instrument safety earth terminal with a green/yellow wire. Do not alter this connection. (Reference: ⏚ or ⚠ stamped inside the rear panel near the safety earth terminal.)

WARNING notes call attention to possible injury or death hazards in subsequent operations.

CAUTION notes call attention to possible equipment damage in subsequent operations.

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SECTION 1
GENERAL DESCRIPTION

1.1 THE MODEL 188

The Wavetek Model 188, 4 MHz Sweep/Function Generator, is a precision source of sine, triangle and square waveforms plus dc voltage. All waveforms are front panel variable from 4 mHz to 4 MHz and can be internally or externally modulated. Frequencies are variable linearly or logarithmically within a frequency range. When used as a sweep generator, an internal ramp generator provides a recurring sweep over a 1000:1 (linear) frequency range or 10,000:1 (logarithmic) frequency range. Output can be continuous or the generator can be triggered or gated by an external signal or a front panel switch. Amplitude of the waveforms is variable form 10V peak-to-peak into 50Ω down to 15 mV peak-to-peak. DC reference of the waveform can be offset positively or negatively.

The two selectable waveform outputs are a 20V peak-to-peak maximum and a 2V peak-to-peak maximum (20 dB down from 20 Vp-p); both may be varied over a 30 dB range. Auxiliary outputs are a TTL level sync, a 600Ω sweep ramp and a 600Ω generator control voltage signal whose level is proportional to the main generator frequency.

1.2 SPECIFICATIONS

1.2.1 Versatility

Waveforms
Sine ∿, triangle △, square ⊓, TTL pulse ⎍ and dc.

Operational Modes
Continuous: Generator runs continuously at selected frequency.

Triggered: Generator is quiescent until triggered by external signal or manual trigger, then generates one complete waveform cycle at selected frequency.

Gated: As triggered mode, except output continues for duration of gate signal. Last waveform started is completed.

Sweep: An internal ramp generator will sweep the main generator from a lower, start frequency to a higher stop frequency, linearly (3 decades) or logarithmically (4 decades).

Sweep Stop: Frequency switches to high sweep limit. Used to set high frequency limit.

Frequency Range
0.004 Hz linear (0.0004 Hz log) to 4 MHz in 7 overlapping decade ranges:

×1	0.004 (0.0004) to 4 Hz
×10	0.04 (0.004) to 40 Hz
×100	0.4 (0.04) to 400 Hz
×1K	4 (0.4) Hz to 4 kHz
×10K	40 (4) Hz to 40 kHz
×100K	400 (40) Hz to 400 kHz
×1M	4 (0.4)kHz to 4 MHz

Function Output
∿, △, ⊓ selectable and variable to 20 Vp-p (10 Vp-p into 50Ω) HI output, and to 2 Vp-p (1 Vp-p into 50Ω) LO output. Both outputs varied with a 30 dB vernier. Peak output current is 100 mA maximum (HI output) into 50Ω (200 mA peak into a short circuit). Source impedance is 50Ω.

DC Offset and DC Output
Waveform offset and dc output selectable and variable through HI and LO BNC outputs. DC output selectable by not selecting a waveform function. HI output is ±10V max (±5V into 50Ω) as offset or Vdc output. Signal-peak plus offset limited to ±10V (±5V into 50Ω). LO output is ±1V max (±0.5V into 50Ω) as is signal-peak plus offset limit. DC offset plus waveform attenuated proportionately at LO (−20 dB) output.

TTL Sync Output
TTL pulse (50% duty cycle) at generator frequency. Drives up to 20 TTL loads.

GCV — Generator Control Voltage
0 to 4.0V open circuit output from 600Ω source impedance. Proportional to frequency of main generator. For use as a horizontal drive signal.

VCG — Voltage Controlled Generator
Up to 1000:1 frequency change (linear mode) or up to 10,000:1 change (logarithmic mode) with external 0 to ±4V signal. Upper and lower frequencies limited to maximum and minimum of selected range.

Slew Rate: 2% of range per μs (linear); 0 to 100% of range in 20 ms (logarithmic).

Linearity: ±0.5% through × 100K range; ±2% on ×1M range.

Input Impedance: 2 kΩ.

Sweep
Main generator is frequency modulated by internal sweep generator. Main generator frequency repeatedly rises from frequency set by dial and range button to frequency set by sweep stop knob.

Sweep Mode: Linear (3 decades max) or logarithmic (4 decades max).

Sweep Rate: 30 ms to 1 min. (nominal) continuously adjustable.

Sweep Width: Up to 1:1000 (linear) or 1:10,000 (logarithmic) continuously adjustable.

Sweep Output
Ramp waveform output with 4V peak into open circuit. Source impedance 600Ω. For use as a horizontal drive signal.

Trigger and Gate
Input: TTL compatible levels.
Pulse Width: 50 ns minimum.
Repetition Rate: 4 MHz maximum.

1.2.2 Frequency Precision

Dial Accuracy
±5% of full scale.

Time Symmetry
Square wave variation from 0.2 to 4.0 on dial less than: ±1% to 100 kHz; ±5% to 4 MHz.

1.2.3 Amplitude Precision

Sine variation with frequency less than: ±0.2 dB on all ranges through ×100K; ±1.0 dB to 4 MHz.

1.2.4 Waveform Characteristics

Sine Distortion
Less than: 0.5% on ×1K and ×10K ranges; 1% on ×1, ×10, ×100 and ×100K ranges. All harmonics 25 dB below fundamental on ×1M range.

Triangle Linearity
Greater than 99% to 200 kHz.

Square Wave Rise and Fall Time
At HI output, less than 50 ns for 10 Vp-p output into 50Ω termination.

1.2.5 General

Environmental
Specifications apply at 25°C ±5°C. Instrument will operate from 0°C to 50°C ambient temperatures.

Dimensions
28.6 cm (11¼ in.) wide; 8.9 cm (3½ in.) high; 26.7 (10½ in.) deep.

Weight
2.7 kg (6 lb) net; 4.5 kg (10 lb) shipping.

Power
90 to 128V or 198 to 264V (specify); 48 to 66 Hz; less than 15 watts.

NOTE

All specifications apply for dial between 0.2 and 4.0; amplitude at 10 Vp-p from HI output into 50Ω termination.

SECTION 2
INSTALLATION

2.1 MECHANICAL INSTALLATION

After unpacking the instrument, visually inspect all external parts for possible damage to connectors, surface areas, etc. If damage is discovered, file a claim with the carrier who transported the unit. The shipping container and packing material should be saved in case reshipment is required.

2.2 ELECTRICAL INSTALLATION

2.2.1 Power Connection

WARNING

To preclude injury or death due to shock, the third wire earth ground must be continuous to the facility power outlet. Before connecting to the facility power outlet, examine extension cords, autotransformers, etc., between the instrument and the facility power outlet for a continuous earth ground path. The earth ground path can be identified at the plug on the instrument power cord; of the three terminals, the earth ground terminal is the nonmatching shape, usually cylindrical.

CAUTION

To prevent damage to the instrument, check for proper match of line and instrument voltage and proper fuse type and rating.

NOTE

Unless otherwise specified at the time of purchase, this instrument was shipped from the factory for operation on a 90 to 128 Vac line supply and with a 1/4 amp slow blow fuse. Instruments configured for 180 to 256 Vac have a 1/8 amp slow blow fuse. Select the appropriate fuse and 115 or 230 switch position at the rear panel when changing power sources.

2.2.2 Signal Connections

Use 3 foot RG58U 50Ω shielded cables equipped with BNC connectors to distribute all input and output signals.

2.3 ELECTRICAL ACCEPTANCE CHECK

This checkout procedure is a general verification of generator operation. Should a malfunction be found, refer to the warranty in the front of this manual.

A two channel oscilloscope, four 3 foot 50Ω coax cables with BNC connectors, a coax tee connector and an additional function generator are required for this procedure.

Preset the generator front panel controls as follows:

Control	Position
Dial	2.0
MODE	CONT (released)
FUNCTION	⎍
DC OFFSET	OFF (ccw)
AMPLITUDE	MAX (cw)
FREQUENCY MULTIPLIER	X 1K
SWEEP	CONT (released)

Set up the oscilloscope, Model 188 and external function generator as shown in figure 2-1 and perform the steps in table 2-1.

2.4 CHANGING THE OUTPUT IMPEDANCE

The output impedance is normally:

HI 10V p-p (50Ω source) into 50Ω.
LO 1V p-p (50Ω source) into 50Ω.

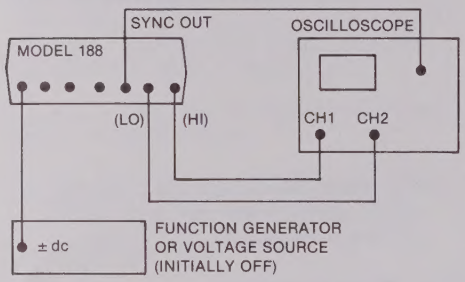

Figure 2-1. First Setup

Amplitude is normally variable over 30 dB for each output with a 50 dB amplitude range available by utilizing both outputs.

If simultaneous 600Ω and 50Ω output impedances are desired:

1. Change value of R148 from 499Ω to 604Ω.
2. Remove R149.

The result is:

HI 10V p-p (50Ω source) into 50Ω.
LO 10V p-p (600Ω source) into 600Ω.

Amplitude is variable over 30 dB. Square wave rise and fall time is less than 150 ns. Any value greater than 600Ω may also be substituted for the value of R148 for other output impedances.

To increase the range of the variable amplitude control in a modified unit beyond 30 dB, decrease the value of R124 as necessary. Waveform quality relative to the standard unit is not guaranteed below −30 dB and above 20 kHz.

Table 2-1. Initial Checkout

Step	Control	Position/Operation	Observation
1	POWER	ON	±10V square wave on CH1 and ±1V on CH2. Return to CH1 only.
2	Dial	Rotate in both directions. Return to 2.0.	Rotation ccw increases frequency of ⎍ ; rotation cw decreases frequency.
3	FREQUENCY MULTIPLIER	Press each switch sequentially; return to ×1K.	Frequency increases in decade steps, left to right.
4	AMPLITUDE	Rotate ccw.	Amplitude decreases.
5	DC OFFSET	Rotate cw. Return to OFF.	Output immediately offset negative, then moves positive. OFF return it to original level.
6	AMPLITUDE	Rotate cw.	Square returns to original amplitude.
7	Function Generator or DC Voltage Source	Vary input dc voltage; then disconnect VCG IN input.	Frequency increases with positive voltage and decreases with negative voltage.
8	FUNCTION	Press ∿ , ⎍ , △ .	Observe ∿ , ⎍ , △ waveforms.
9	MODE	Gate (CONT depressed, TRIG/GATE released).	A dc level near zero volts (except ⎍ function).
10	MANUAL TRIGGER	Press and hold.	Continuous △ .

Table 2-1. Initial Checkout (Continued)

Step	Control	Position/Operation	Observation
Set up trigger source as shown in figure 2-2. Set trigger source for 100 Hz TTL signal.			
11	——	———	∿ gated on during positive portion of TTL signal on CH2.
12	TRIG/GATE	Trigger (depressed)	One cycle per trigger cycle.
13	MODE	Main generator continuous (CONT released)	Setup connectors as shown in figure 2-3. Sync scope on channel 2 input.
14	Dial	Full cw	
15	SWEEP Controls	Linear sweep (CONT depressed, SWP/STOP depressed, LOG/LIN extended, STOP full cw, TIME centered)	Output varies from low frequency to high frequency
16	LIN/LOG Button	Press	Logarithmic distributed sweep when compared to step 15 linear sweep.

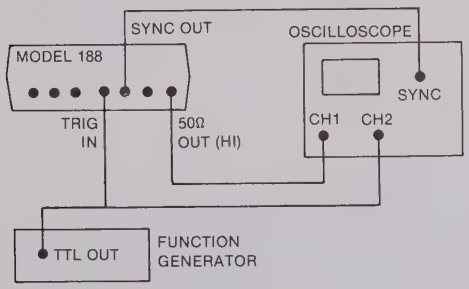

Figure 2-2. Second Setup

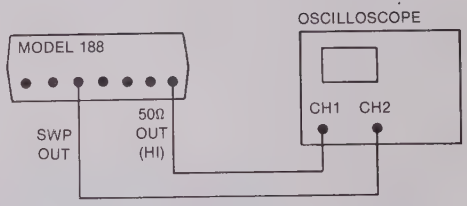

Figure 2-3. Third Setup

SECTION 3
OPERATION

3.1 CONTROLS AND CONNECTIONS

The generator front panel controls and connectors are shown in figure 3-1 and keyed to the following descriptions.

1 **Frequency Dial** — Settings under the dial index mark summed with **21** and multiplied by **4** determine the output signal frequency and the sweep start frequency in sweep mode. The dial is engraved with both linear and logarithmic scales: outer scale linear and inner scale logarithmic.

2 **POWER Button** — Turns generator ON and OFF.

3 **STOP Knob** — Sets the upper frequency limit when CONT **5** is depressed and SWP/STOP **6** is extended.

4 **FREQUENCY MULTIPLIER Controls** — Selects one of seven frequency multipliers for dial **1** setting.

5 **CONT Button** — Selects sweep submode to main generator's continuous mode. Extended is continuous (nonsweep) mode while depressed is sweep mode. Sweep is from a low frequency set by **1** to a high frequency set by **3**. Main generator mode control **8** must be in continuous mode (extended).

6 **SWP/STOP Button** — When button is depressed (and **5** depressed and **8** extended) selects repetitive sweep of the main generator frequency. When button is extended, the frequency is stopped at the upper sweep limit with upper frequency being set by STOP control **3**.

7 **LIN/LOG Button** — Selects linear or logarithmic frequency distribution of sweep,

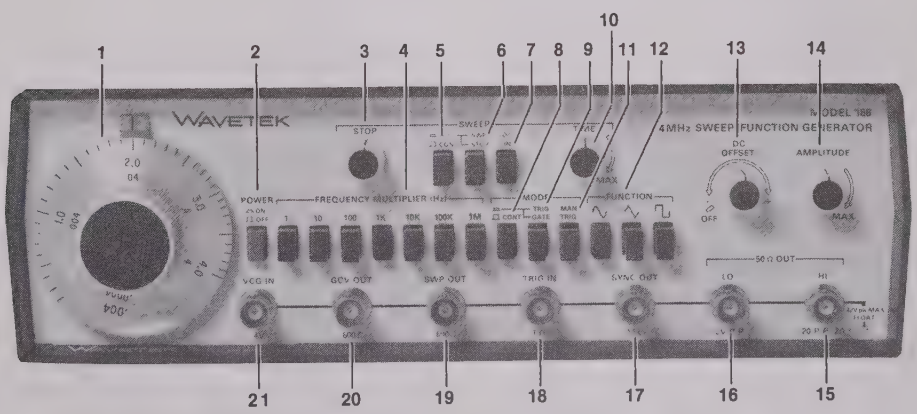

Figure 3-1. Controls and Connectors

VCG and main dial within a frequency range. Linear operation is selected when the button is extended. Logarithmic operation is selected when the button is depressed.

8, 9 Generator MODE Controls — Selects one of the following three modes:

CONT — **8** released. Continuous output at 50Ω OUT **15** and **16** and SYNC OUT (TTL) **17** connectors.

TRIG — **8** and **9** pressed. DC level output until generator triggered by the MAN TRIG **11** or with a signal at the TRIG IN connector **18** . When triggered, the generator output is one cycle of waveform followed by a dc level.

GATE — **8** pressed and **9** released. As for TRIG except the output is continuous for the duration of the manual or external trigger signal. The last cycle started is always completed.

10 TIME Knob — Sets the sweep time by controlling the period of the sweep ramp generator.

11 Manual Trigger Button — Triggers or gates the output signals when generator mode is TRIG or GATE (**8** pressed). In trigger mode, one waveform cycle is output when the button is pushed. In gate mode, waveform cycles are continuously output as long as the button is held in.

12 FUNCTION Selector — Selects one of three waveforms or when all three buttons are released, a dc level.

13 DC OFFSET Control — Offsets the 50Ω OUT waveforms or gives dc levels from −10V to +10V (−5V to +5V into 50Ω) at **15** and from −1V to +1V (−0.5V to +0.5V into 50Ω) at **16**. An OFF position ensures no offset.

14 AMPLITUDE Control — Ccw rotation reduces waveform amplitudes at **15** and **16** by 30 dB. DC and offset voltages are not affected by this control.

15 50Ω OUT HI Connector — The main output of the generator at the function selected. Maximum 20 Vp-p (10 Vp-p into 50Ω) with 30 dB continuous amplitude control. 50Ω source impedance.

16 50Ω OUT LO Connector — Same as **15** except 20 dB (1/10) lower in amplitude.

17 TTL OUT Connector — A TTL square for each cycle of the generator. To be used for synchronization or as a TTL signal capable of driving 20 TTL loads.

18 TRIG IN Connector — Accepts a TTL signal to trigger or gate the generator. Triggers on the rising (low to high) transition and gates during the positive (high) portion of the triggering signal.

19 SWP OUT Connector — Supplies a ramp waveform with an approximate 4V peak into an open circuit. For use as a horizontal drive signal. Source impedance is 600Ω.

20 GCV OUT Connector — Provides a 0 to 4V open circuit output proportional to the frequency of the main generator. For use as a horizontal drive signal. Source impedance is 600Ω.

21 VCG IN Connector — Accepts ac or dc voltages to proportionately control frequency within the range determined by the FREQUENCY MULTIPLIER **4** . Positive voltage increases the frequency set by the dial **1** ; negative voltage decreases the frequency. The VCG IN will not drive the generator frequency beyond the normal dial limits of a range. Input impedance is 2 kΩ.

3.2 OPERATION

Perform the initial checkout in Section 2 for the feel of the instrument. Any questions concerning individual controls and connectors may be answered in paragraph 3.1.

3.2.1 Signal Termination

Proper signal termination, or loading, of the generator connectors is necessary for its specified operation. For example, the proper termination of either of the 50Ω OUT connectors is shown in figure 3-2. Placing the 50Ω terminator, or 50Ω resistance, in parallel with a higher impedance, matches the receiving instrument input impedance to the coax characteristic and generator output impedance, thereby minimizing signal reflection or power loss on the line due to impedance mismatch.

The input and output impedances of the generator connectors are listed below.

Connector	Impedance
50Ω OUT (HI)	50Ω
50Ω OUT (LO)	50Ω
SYNC OUT (TTL)	*
TRIG IN	*
VCG IN	2 kΩ
SWP OUT	600Ω
GCV OUT	600Ω

*The TTL OUT connector is diode protected and can drive up to 20 Transistor-Transistor-Logic (TTL) loads (low level between 0V and 0.4V, and high level between 2.4V and 5V). It should not be connected to resistive load less than 600Ω. The TRIG IN connector accepts TTL logic levels, is diode protected, and requires 500 μA drive from a high level output.

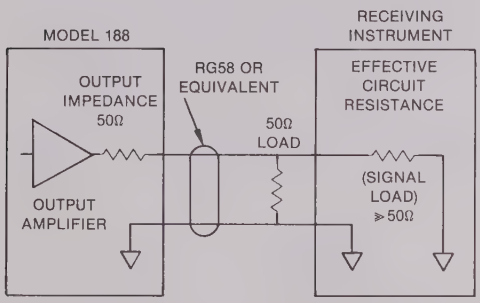

Figure 3-2. Signal Termination

3.2.2 Manual Function Generator Operation

For basic operation, select the waveform frequency and amplitude. The following steps demonstrate manual control of the function generator. (Bold numbers are keyed to figure 3-1.)

Step	Control/Connector	Setting
1	50Ω OUT **15** **16**	Connect circuit to either output (refer to paragraph 3.2.1).
2	FREQUENCY MULTIPLIER **4**	Set to desired range of frequency.
3	Frequency Dial **1**	Set to desired frequency within the range.
4	SWEEP's CONT **5**	Extended.
5	FUNCTION **12**	Set to desired waveform.
6	DC OFFSET **13**	Set as desired. Limit waveform amplitude to prevent clipping (see figure 3-3).
7	AMPLITUDE **14**	Set for desired amplitude.

3.2.3 Voltage Controlled Function Generator Operation

Operation as a voltage controlled function generator (VCG) is as for a manually controlled function generator, only the frequency within a particular range is additionally controlled by an external voltage (±4V excursions) injected at the VCG IN connector. Perform the steps given in paragraph 3.2.2, only set the frequency dial to determine a reference from which the frequency is to be voltage controlled:

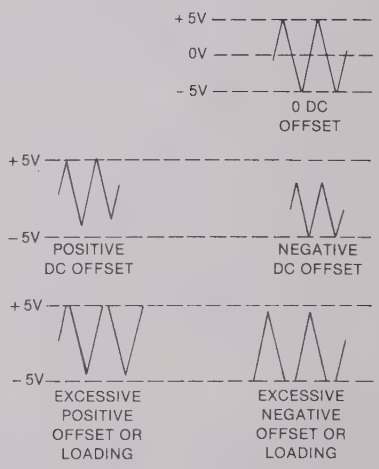

Figure 3-3. DC OFFSET Control

1. For frequency control with positive dc inputs at VCG IN, set the dial for a lower frequency limit.
2. For frequency control with negative dc inputs at VCG IN, set the dial for an upper frequency limit.
3. For modulation with an ac input at VCG IN, set the dial at the desired center frequency. Do not

exceed the limits of the selected frequency range.

Figure 3-4 is a nomograph with examples of dial and voltage effects. Example 1 shows that with 0V VCG input, frequency is determined by the main dial setting, 2 (linear mode) or .04 (logarithmic mode) in this example. Example 2 shows that with a positive VCG input, output frequency is increased. Example 3 shows that with a negative VCG input, output frequency is decreased. (Note that the Output Frequency Factor column value must be multiplied by a frequency range multiplier to give the actual output frequency.)

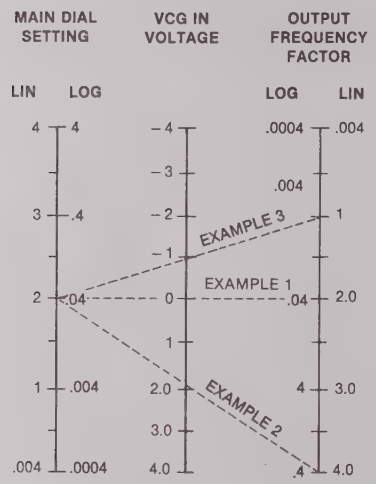

Figure 3-4. VCG Voltage-to-Frequency Nomograph

NOTE

Nonlinear operation may result when the VCG input voltage is excessive; that is, when the attempted generator frequency exceeds the range limits. The upper limit is four times the multiplier setting, and the lower limit is 1/1000th (linear) or 1/10,000 (logarithmic) of the upper limit.

The up to 1000:1 (linear) or 10,000:1 (logarithmic) VCG sweep of the generator frequencies available in each range results from a 4V excursion at the VCG IN connector. With the frequency dial set to 4.0, excursions between −4V and 0V at VCG IN provide the up to 1000:1 (lin) or 10,000:1 (log) frequency sweep. With the dial set to .004 (linear) or .0004 (logarithmic), excursions between 0V and +4V at the VCG IN provide up to 1000:1 (linear) or 10,000:1 (logarithmic sweep within the set frequency range.

3.2.4 Sweep Generator Operation

Operation as a sweep generator is similar to manually controlled generator operation except the main generator can be repetitively swept between two selected frequencies either linearly or logarithmicly at a selected sweep rate. The relationship of internal ramp and main generator is shown in figure 3-5.

The following steps describe the sweep operation setup.

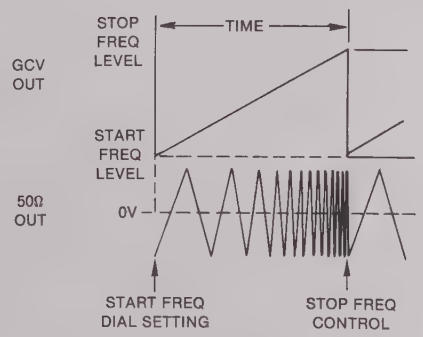

Figure 3-5. Effect of Sweep Time and Width on Output Frequency

Step	Control	Setting
1	MODE: CONT **8**	Press to release. (Continuous mode of main generator is necessary for sweep.)

Step	Control	Setting
2	Frequency dial **1**	Select sweep start frequency.
3	SWEEP's CONT **5**	Depressed. (Selects sweep submode of main generator's continuous operation.)
4	SWP/STOP **6**	Press to release. (Extended allows setting of stop frequency.)
5	STOP **3**	Select the stop frequency. (The stop frequency will always be higher than the start frequency.)
6	Time **10**	Sets the internal sweep rate.

3.2.5 Waveforms

Waveform timing for each mode of operation is shown in figure 3-5.

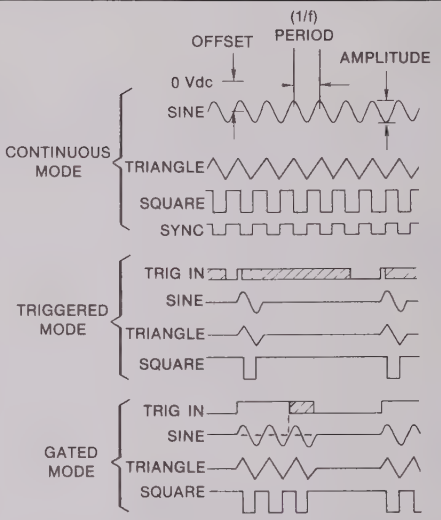

Figure 3-5. Waveform Characteristics

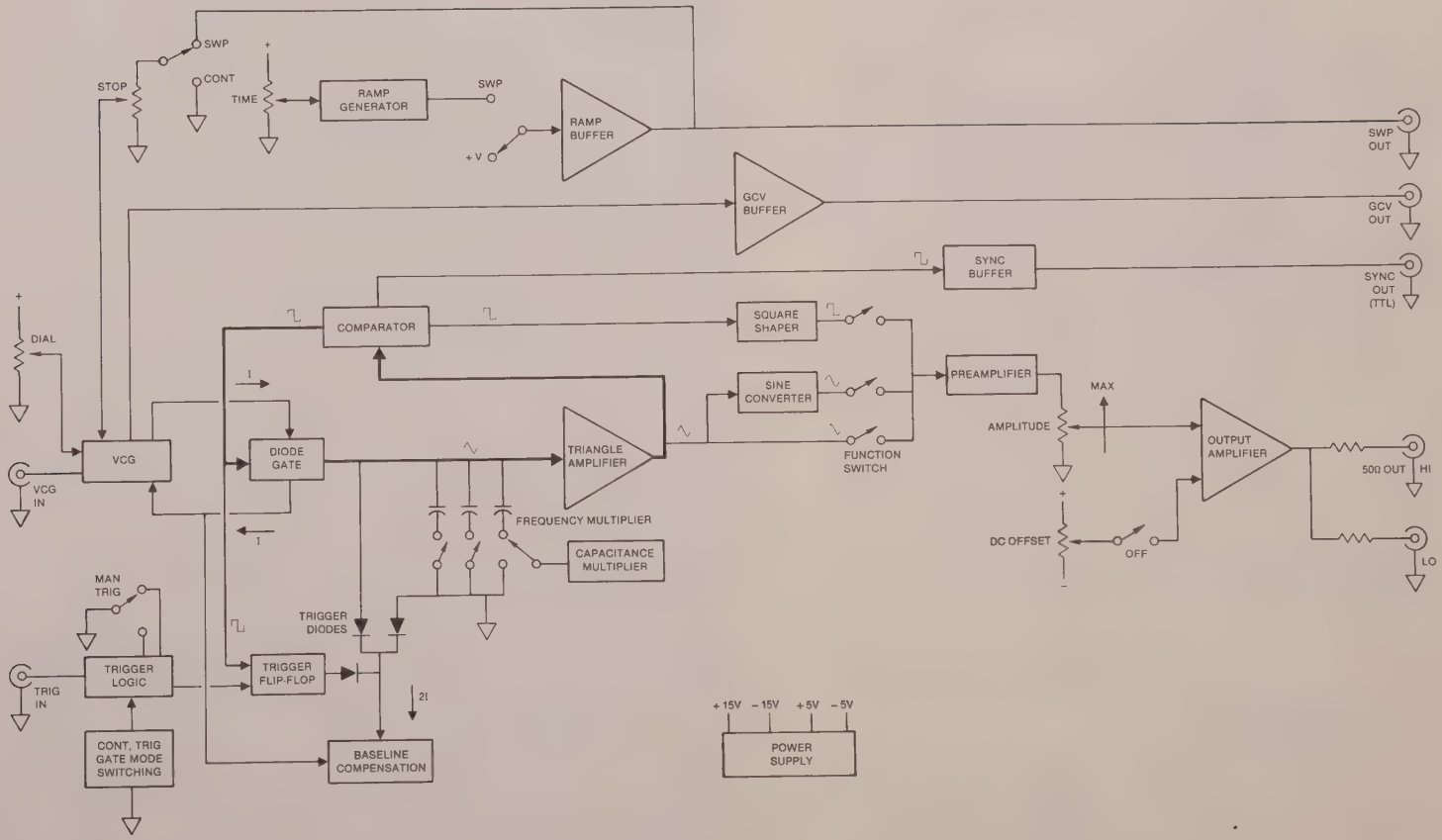

Figure 4-1. Function Block Diagram

SECTION 4
CIRCUIT DESCRIPTION

This section describes the functions of major circuit elements and their relationships to one another as shown in figure 4-1, functional block diagram. The following sections in this manual provide more detailed information for maintaining the instrument.

As shown in figure 4-1, the VCG (Voltage Control of Generator) sums voltage inputs from the frequency dial and the VCG IN connector. This sum voltage controls the magnitude of a complementary current source and current sink. This current varies linearily from approximately 2 mA to 2 μA over a 1000:1 (4.0 to .004) range or logarithmically from approximately 2 ma to 0.2 μa over a 10,000:1 (4.0 to .0004) range of each frequency multiplier. The VCG also controls the trigger baseline compensation circuit, which consists of another current sink at twice the current magnitude.

The diode gate, controlled by the comparator output, connects either the current source or the current sink to the timing capacitor selected by the frequency multiplier. When the current source is switched in, the charge on the timing capacitor will rise linearily, producing the positive-going triangle slope. Likewise, the current sink produces the negative-going triangle slope.

The triangle amplifier is a unity gain amplifier whose output is fed to the comparator and to the output circuits. The comparator operates as a window detector with limit points set to the triangle peaks. The ±2V output is sent back to the diode gate and to the output circuits. When the output is +2V, the triangle is positive-going until the +1.25V limit is reached and the comparator output switches to −2V. When the output is −2V, the triangle is negative-going until the −1.25V limit is reached and the comparator output switches back to +2V, repeating the process. In this manner, the basic function generator loop, the bold path in figure 4-1, produces simultaneous generation of triangle and square waves at the same frequency.

The output frequency is determined by the magnitude of the timing capacitor selected by the frequency multiplier switches and by the magnitude of the currents supplied to and removed from it. Since the currents are linearily proportional to the sum of the VCG inputs, so will be the output frequency.

To extend the lower frequency capability of the generator, a capacitance multiplier circuit divides VCG currents by 10 (effectively multiplying the timing capacitor by 10) for each of the lower 3 multiplier ranges.

The TTL square from the comparator is buffered and sent to the SYNC OUT TTL connector. The other side is sent to the trigger flip-flop and to a level shifter to produce the ±2V bipolar square for the diode gate and the square shaper circuits. The square shaper converts the square into a current signal and applies it to the ⎍ FUNCTION switch. The buffered triangle is applied to the ∿ FUNCTION switch and to the sine converter input. The sine converter, using the nonlinear characteristics of its diodes, converts the triangle into a sinusoidal current for the ∿ FUNCTION switch.

The selected function is sent to the preamplifier, where it is inverted and buffered. The preamplifier output goes to the output amplifier through the AMPLITUDE control where it is summed with offset voltage from the DC OFFSET control. Here, waveform and offset are inverted and amplified to a ±10V peak signal which can drive a 50Ω termination from a 50Ω source impedance. The output amplifier drives the 50Ω OUT HI connector and a resistor divider producing the 50Ω OUT LO output.

Noncontinuous modes of operation (trigger and gate) result from allowing or preventing the VCG current source from charging the timing capacitor. Whenever the trigger flip-flop output is low, each of the two trigger diodes conduct a current I, sourcing 2I to the baseline compensation circuit. This removes the current I from the VCG current source and forces a 0V baseline at the triangle amplifier input.

When the CONT switch is released, trigger logic is inhibited from passing any trigger signals and the trigger flip-flop output is held high. This prevents the trigger diodes from conducting and the generator loop operates continuously.

When the CONT switch is pressed, the generator loop is held at the 0V baseline. Pressing the TRIG/GATE switch puts the instrument in triggered mode and any external or manual trigger signals at the trigger logic input will be transformed into a narrow pulse corresponding to the low-to-high transition of the trigger input. This pulse sets the trigger flip-flop high and allows the generator loop to run. When the triangle negative peak is reached, the comparator low-to-high transition clocks the trigger flip-flop low and, when the 0V baseline level is reached, the generator loop again stops. The result is a single cycle generated after the triggering signal corresponding to 0 to 360° of phase. Successive triggered waveforms always start at the same 0° point.

Releasing the TRIG/GATE switch puts the instrument in the gated mode. This is identical to the triggered mode, except the trigger flip-flop is held high for the full duration of the triggering signal. The generator produces continuous waveforms during the time the external signal is high or the manual trigger switch is held in. The last triggered cycle started is always completed and successive gated bursts always start at the 0° point.

When sweep mode is selected by a combination of the main generator in continuous mode and the ramp generator switches set to SWP, the ramp generator is enabled and a ramp voltage becomes part of the control voltage in the VCG circuit to control the main generator frequency. Ramp period, variable from 30 ms to 1 minute, is set by the TIME Control. Ramp generator output is buffered to drive the sweep output and VCG circuit. The ramp magnitude suppling the VCG input is controlled by the STOP potentiometer.

Selecting the stop switch position biases the buffer amplifier to a level equal to the positive peak of the ramp (+V). In this static mode the upper sweep limit can be set by the STOP Control.

When the CONT position of the SWEEP switch is selected the ramp generator is disabled and the buffered ramp is disconnected from the VCG input.

The GCV (Generator Control Voltage) from the VCG circuit is a resultant voltage from the three VCG inputs: dial, VCG IN and sweep ramp. This voltage is buffered and made available at the GCV BNC.

SECTION 5
ALIGNMENT

5.1 FACTORY REPAIR

Wavetek maintains a factory repair department for those customers not possessing the necessary personnel or test equipment to maintain the instrument. If an instrument is returned to the factory for alignment or repair, a detailed description of the specific problem should be attached to minimize turnaround time.

5.2 REQUIRED TEST EQUIPMENT

Voltmeter . Millivolt dc measurement (1% accuracy)
Oscilloscope . . . . . ≥60 MHz bandwidth
Counter . . . . . 4 MHz (0.1% accuracy)
50Ω Feedthru . . . . . ±1% accuracy, 2W
Distortion Analyzer . . . . . To 400 kHz
RG58U Coax Cable . . . 3 ft length BNC male contacts

5.3 REMOVING GENERATOR COVERS

1. Invert the instrument and remove the four screws in the bottom cover.

2. Turn the instrument upright; remove the top cover for access to generator alignment controls.

3. When alignment is complete, secure the bottom cover with four screws.

NOTE

Remove the cover only when it is necessary to make adjustments or measurements.

5.4 ALIGNMENT

After referring to the following preliminary data, perform alignment, as necessary, per table 5-1. If performing partial alignment, check previous settings and adjustments for applicability. See figures 5-1 and 5-2 for alignment control location.

1. All measurements made at the FUNCTION OUT connector must be terminated into a 50Ω (±1%) load.

2. Start the alignment by connecting the unit to an appropriate ac power source and setting the front panel switches as follows.

POWER . . . . . ON
Frequency Dial 4.0
FREQ MULT (Hz) . . . . . ×1K
MODE CONT . . . . . CONT (released)
FUNCTION . . . . . ⊓
DC OFFSET . . . . . OFF
AMPLITUDE . . . . . MAX
LIN/LOG . . . . . LIN (released)
SWEEP's CONT . . . . . CONT (released)
SWP/STOP . . . . . STOP (released)

3. Allow the unit to warm up at least 30 minutes for final alignment. Keep the instrument cover on to maintain heat. Remove cover only to make adjustments or measurements.

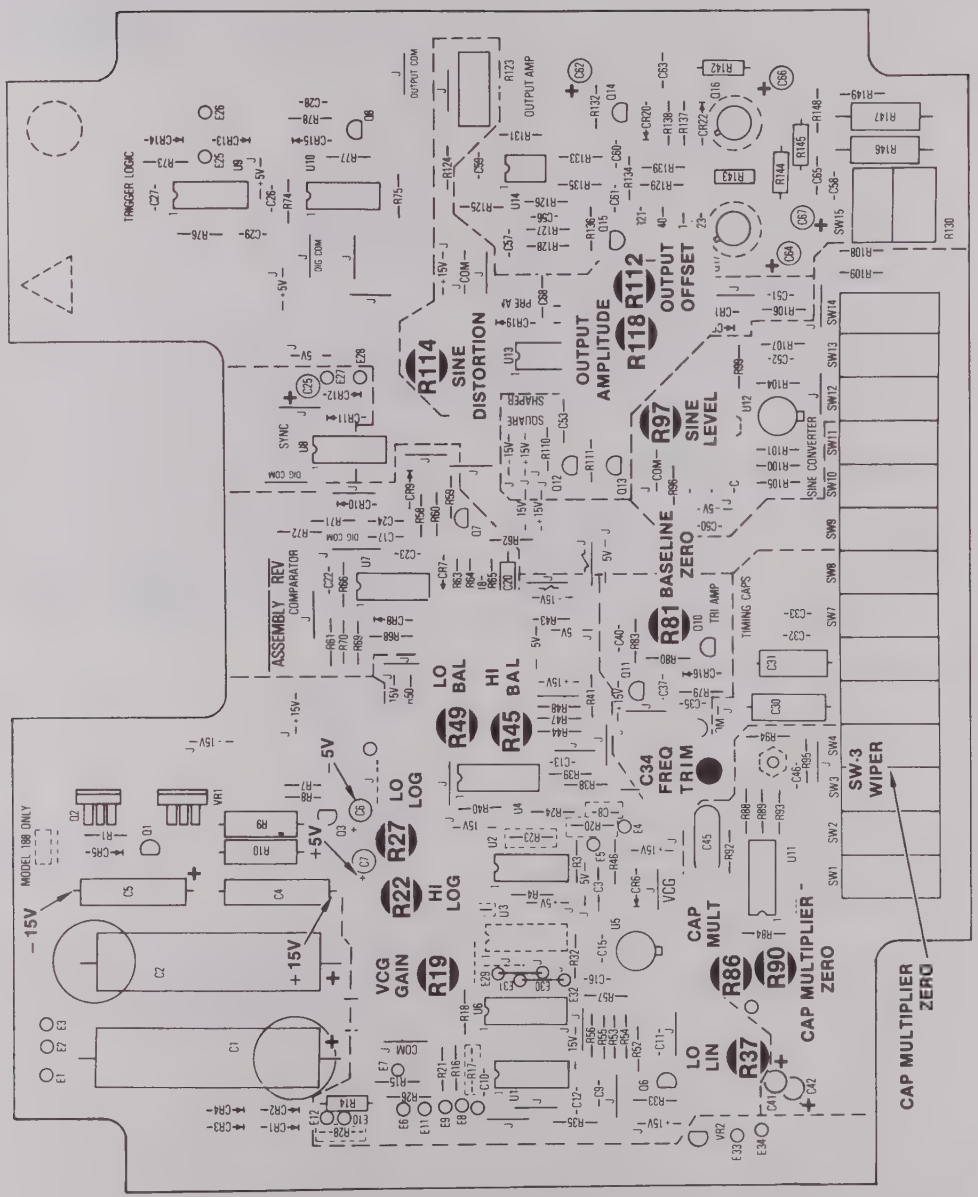

Figure 5-1. Alignment Point Location, Generator Board

Table 5-1. Alignment Procedure

Step	Check	Tester	Test Point	Control Setting	Adjust	Result	Remark
1	Power Supply	Voltmeter	C4 +	Paragraph 5.4, Step 2		+ 15 ± .75V	Verify ± 15V should track within 30 mV
2			C5 –			– 15 ± .75V	
3			C7 +			+ 5 ± .25V	Verify
4			C6 –			– 5 ± .25V	
5	Capacitor Multiplier Zero		SW3 - B Wiper		R90 CAP MULT ZERO	0 ± 2V	
6	Approximate Bottom of the Dial Frequency	Counter	50Ω OUT HI (terminate into 50Ω	Dial: .004 FREQ MULT: 10K	R37 LO LIN	20 to 25 ms period	
7	Bottom of the Dial Symmetry	Scope			R49 LO BAL	Equalize (+) and (–) half cycles	Set scope to (–) trigger; display one full cycle. Align positive transition to center of screen. Multiply the horizontal display × 10. Set scope to (+) trigger; adjust R49 to align negative transition with center of screen
8	Bottom of the Dial Frequency (Lin)	Counter		FREQ MULT: × 1K	R37 LO LIN	350(± 50) ms period	
9	Top of the Dial Symmetry	Scope		Dial: 4.0	R45 HI BAL	Equalize (+) and (–) half cycles	See step 7
10	Top of the Dial Frequency (Lin)	Counter		Dial: 4.0 FREQ MULT: × 1K	R19 VCG GAIN	4 ± .2 kHz	
11				FREQ MULT: × 10K		40 ± .8 kHz	Verify
12				FREQ MULT: × 1M	C34 FREQ TRIM	4 ± .02 MHz	
13				FREQ MULT: × 100K		400 ± 8 kHz	V e r i f y . If necessary, trim by changing value of C33
14				FREQ MULT: × 100	R86 CAP MULT	2.5 ± .05 ms	

Table 5-1. Alignment Procedure (Continued)

Step	Check	Tester	Test Point	Control Setting	Adjust	Result	Remark
15				FREQ MULT: ×10		25 ±.5 ms	Verify
16				FREQ: ×1		250 ±5 ms	
17	Bottom of the Dial Frequency (Log)			Dial: .0004 FREQ MULT: ×100K SWEEP: CONT, STOP, LOG	R27 LO LOG	40 ±2 Hz	Allow 1 hour warm-up
18	Top of the Dial Frequency (Log)			Dial: 4.0 FREQ MULT: ×100K	R22 HI LOG	400 ±10 kHz	Repeat steps 17 and 18 once.
19	Sine Distortion (Lin)	Distortion Analyzer		FUNCTION: ∿ FREQ MULT: ×1K	R97 SINE LEVEL R114 DISTORTION	Adjust for minimum distortion	It may be necessary to reduce amplitude to 5V peak.
20	Output Amplitude	Scope		FUNCTION: ∿	R118 OUTPUT AMPL	10 Vp-p (+.3V/−0V)	
21	Output Offset	Voltmeter		FUNCTION: ∿	R112 OFFSET	0 ±50 mV	
22	Baseline Zero	Scope		MODE: Trigger	R81 BASE-LINE ZERO	0 ±75 mV	It may be necessary to trim the baseline with R80
23	Sweep Offset	Voltmeter	SWP OUT (Unterminated)	SWEEP: CONT, SWP, LIN	R9 (Sweep board) SWEEP OFFSET	0 ±2 mV	

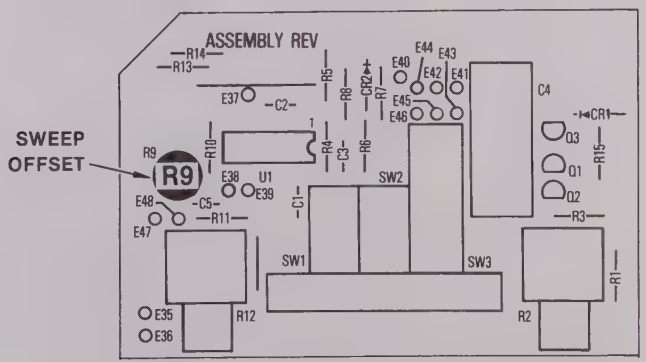

Figure 5-2. Alignment Point Location, Sweep Board

SECTION 6
TROUBLESHOOTING

6.1 FACTORY REPAIR

Wavetek maintains a factory repair department for those customers not possessing the necessary personnel or test equipment to maintain the instrument. If an instrument is returned to the factory for alignment or repair, a detailed description of the specific problem should be attached to minimize turnaround time.

6.2 TROUBLESHOOTING TABLES

Table 6-1 gives an index of the troubleshooting tables by indications of common problems. The tables do not cover every possible trouble, but, when used in conjunction with circuit descriptions and schematics, will be an aid in systematically isolating faulty components.

6.3 TROUBLESHOOTING INDIVIDUAL COMPONENTS

6.3.1 Transistor

1. A transistor is defective if more than one volt is measured across its base-emitter junction in the forward direction.

2. A transistor when used as a switch may have a few volts reverse bias voltage across base-emitter junction.

3. If the collector and emitter voltages are the same, but the base emitter voltage is less than 500 mV forward voltage (or reversed bias), the transistor is defective.

4. A transistor is defective if its base current is larger than 10% of its emitter current (calculate currents from voltage across the base and emitter series resistors).

5. In a transistor differential pair (common emitter stages), either their base voltages are the same in normal operating condition, or the one with less forward voltage across its base emitter junction should be off (no collector current); otherwise, one of the transistors is defective.

6.3.2 Diode

A diode (except a zener) is defective if there is greater than one volt (typically 0.7 volt) forward voltage across it.

6.3.3 Operational Amplifier

1. The "+" and "−" inputs of an operational amplifier will have less than 15 mV voltage difference when operating under normal conditions.

2. When the output of the amplifier is connected to the "−" input (voltage follower connection), the output should be the same voltage as the "+" input voltage; otherwise, the operational amplifier is defective.

3. If the output voltage stays at maximum positive, the "+" input voltage should be more positive than the "−" input voltage, or vice versa; otherwise, the operational amplifier is defective.

6.3.4 FET Transistor

1. No gate current should be drawn by the gate of an FET transistor. If so, the transistor is defective.

2. The gate-to-source voltage is always reverse biased under a normal operating condition; e.g., the source voltage is more positive than the gate voltage for 2N5485, and the source voltage is more negative than gate voltage for a 2N5462. Otherwise, the FET is defective.

3. If the device supplying gate voltage to an FET saturates, the FET has too large a Vgs (pinch off) for the circuit and should be replaced.

6.3.5 Capacitor

1. Shorted capacitors have zero volts across their terminals.

2. Opened capacitor can be located (but not always) by using a good capacitor connected in parallel with the capacitor under test and observing the resulting effect.

6.4 GENERAL INSTRUCTIONS

When encountering a problem, it is advisable to return as many of the front panel controls as possible to their initial settings and still retain the problem. The troubleshooting tables in this section generally begin at these initial settings and specify all subsequent setups. Preset the front panel controls as follows.

Control	Position
Frequency Dial	4.0
POWER	ON
FREQ MULT (Hz)	1K
FUNCTION	∿
DC OFFSET	OFF
AMPLITUDE	MAX
SWEEP	CONT, STOP, LIN

CAUTION

To prevent damage to components, turn unit off while removing or replacing components, connectors or pc boards.

The suspected malfunctioning condition should be double checked to eliminate the possibility of improper settings or connections. Before attempting fault isolation, the unit should be checked for proper line voltage selection (refer to Section 2). A good visual inspection of the boards and chassis wires for damage or overheating often saves much time.

Once the malfunction is defined, begin the isolation procedure by selecting an indication in table 6-1 which best describes the malfunction and proceed to the referenced troubleshooting table.

Follow through the checks in the troubleshooting table, using schematics and assemblies as a guide. When positive results are not obtained, perform the indicated corrective procedure.

Table 6-1. Fault Isolation

Indication	Table
1. Fuse blown, no power indication or no outputs.	6-2
2. Function outputs missing or clipped when TTL sync OK. Triangle problem.	6-3
3. Sine waveform problem.	6-4
4. Square waveform problem.	6-5
5. TTL sync output problem.	6-6
6. Generator frequency does not respond correctly to dial and VCG input.	6-7
7. Waveform symmetry problem.	6-8
8. Problem on bottom three ranges only.	6-9
9. Generator trigger and gate mode problem.	6-10
10. Sweep problem.	6-11

Table 6-2. Power Supplies and Generator Loop

Indication: Fuse blown, no power indication or no outputs.

Check	Corrective Procedure
1. Set all controls in their initial positions (refer to paragraph 6.4).	
2. Ensure line voltage matches instrument configuration (refer to Section 2). Check fuse.	Replace fuse; check for normal operation.
3. Check C1 (+) and C2 (−) for ±20 to 26V unregulated dc.	a. CR1 - CR4. b. C1, C2. c. SW1. d. T1, RV1, F1 (bracket assembly).

Table 6-2. Power Supplies and Generator Loop (Continued)

Indication: *Fuse blown, no power indication or no outputs.*	
Check	**Corrective Procedure**
4. Check indicator lamp.	DS1 and VR2, wiring E34 and E33.
5. Check C4 (+) for +15 Vdc.	a. VR1. b. Excessive loading; use board jumpers to isolate cause.
6. Check C5 (−) for −15 Vdc.	a. Q2. b. U2, Q1. c. Excessive loading; use board jumpers to isolate cause.
7. Check U7 pin 14 for +5 Vdc and U7 pin 13 for −5 Vdc.	a. Q4, Q3, U2. b. Excessive loading; use board jumpers to isolate cause.
8. Check U4 pin 6 for a dc shift from approximately +10V to +15V as the frequency dial is rotated from 4.0 to .004. Check U6 pin 8 for a dc shift from −10Vto −15V as the frequencydial is rotated from 4.0 to .004.	Go to table 6-7.
9 Check anode CR6 for approximately +3.5 Vdc.	Go to table 6-10.
10. If emitter Q11 has a 4 kHz, ±1.25V triangle, go to table 6-3.	
11. Check for the same voltage at the gate of Q9 as at the emitter of Q11, within saturation limits of the amplifier.	Q9 - Q11 and associated circuitry.
12. If the voltage at the emitter of Q11 is ≥ +1.25V, check cathode CR10 for approximately −2.5V. If the voltage at the emitter of Q11 is ≤ −1.25V, check cathode CR10 for approximately +2.5V.	U7, Q7 and associated circuitry.
13. Check U5.	

Table 6-3. Output Circuits

Indication: *Function outputs missing or clipped when TTL sync output OK. Problem with triangle waveform.*	
Check	**Corrective Procedure**
1. Set controls to initial positions (refer to paragraph 6.4).	Check for normal operation.
2. Check emitter Q11 for a 4 kHz, ±1.25V triangle.	Go to table 6-2.
3. Select triangle function, rotate AMPLITUDE ccw, and check U13 pin 10 for a ±1.25V triangle.	a. R114 R118 adjustments. b. U13. c. SW13.

Table 6-3. Output Circuits (Continued)

Indication: Function outputs missing or clipped when TTL sync output OK. Problem with triangle waveform.

Check	Corrective Procedure
4. Rotate AMPLITUDE cw (MAX), DC OFFSET to OFF, and check 50Ω OUT (HI) for a 20V p-p (open circuit) triangle.	a. Output amplifier circuit. b. E15, E16 wiring.
5. Check for excessive discontinuities at the triangle peaks near the bottom of a frequency range (other than ×1 to ×100).	a. U5. b. SQR signal at cathode CR10 not ±2.5V.
6. Check for nonlinearities in the triangle slopes near the bottom of a frequency range (other than ×1 to ×100).	a. Associated timing capacitor or C36. b. U5, CR6. c. Q9, Q10.
7. Check for a waveform symmetry problem.	Go to table 6-8.

Table 6-4. Sine Conversion

Indication: Sine waveform problem.

Check	Corrective Procedure
1. Set controls to initial positions (refer to paragraph 6.4).	Check for normal operation.
2. Check emitter Q11 for a 4 kHz, ±1.25V triangle.	Go to table 6-2.
3. Verify that the ±1.25 triangle peaks at the emitter of Q11 agree within 3%.	a. R62, R63, R64, R65, R67, R68, R70. b. CR8, CR9, U7. c. ±15V supplies.
4. Select triangle function; check for ±1.25V triangle at U13 pin 10.	Go to table 6-3, step 3.
5. Select sine function; check for ±1.25V sine at U13 pin 10.	a. U12 circuitry. b. SW12.
6. Check sine distortion 50Ω OUT (HI) per calibration procedure (refer to table 5.1).	a. R97, R114 adjustments. b. Waveform symmetry, R45 adjustment and table 6-8. c. U12 circuitry.
7. Check sine amplitude vs frequency per specifications (refer to section 1).	C47, C55, C56, C57.

Table 6-5. Square Function

Indication: Square waveform problem.

Check	Corrective Procedure
1. Set controls to initial positions (refer to paragraph 6.4).	Check for normal operation.
2. Check CR10 cathode for a 4 kHz, approximately ±2V square wave.	Go to table 6-2.
3. Select a triangle function; check U13 pin 10 for a ±1.25V triangle.	Go to table 6-3.
4. Select square function; check U13 pin 10 for a ±1.25V square.	a. Q12, Q13 circuitry. b. SW14.
5. Check square wave at 50Ω OUT (HI) for the same 20V p-p (open circuit) amplitude as the triangle and sine.	R106, R110, R111.
6. Check rise/fall times of 4 MHz square (50Ω terminated) for <50 ns.	C51, C55, C56, C57.

Table 6-6. TTL Sync Output

Indication: TTL sync output problem.

Check	Corrective Procedure
1. Set controls to initial positions (refer to paragraph 6.4).	Check for normal operation.
2. Check U8 pin 1 for a TTL level, 4 kHz square.	Go to table 6-2.
3. Check U8 pin 8 for a TTL level, 4 kHz square.	a. U8. b. CR11, CR12.
4. Check SYNC OUT TTL.	E27, E28, E19 wiring.
5. Check SYNC OUT waveform at 4 MHz, using a TTL load termination or a ≥600Ω resistive termination and ≤3 foot RG58U coax.	a. U8. b. E19 ground connection.

Table 6-7. VCG Circuit

Indication: Generator does not respond correctly to dial and VCG input.

Check	If Faulty, Check
1. Set controls to initial positions (refer to paragraph 6.4).	Check for normal operation.
2. Check for approximately +15V at E11.	a. E10, E11 and E12 wiring. b. +15V supply. c. Dial potentiometer.
3. Check for 0 ±5 mV at U1 pin 13.	U1.
4. Check U1 pin 14 for approximately −5V.	U1.
5. Check that as the dial is rotated from 4.0 to .004, the voltage at U1 pin 14 varies from approximately −5 to 0V.	U1.
6. Ensure that U1 pin 5 remains at a constant 0V ±40 mV as the dial is varied.	U1, U4, and U6 circuits.
7. Check that, as the dial is rotated from .004 to 4.0, the voltage at U1 pin 1 does not saturate near −15V or +15V (typical range is between −10V and +10V) and stops varing with the dial.	Q6, U1, and U6 circuits.
8. Check that as the dial is rotated from .004 to 4.0, U6 pin 8 varies from approximately −15V to −10V.	U6, U1, and Q6 circuits.
9 Check that, as the dial is rotated from .004 to 4.0, the voltage at U1 pin 7 does not saturate near +15V or −15V (typical range is between +10V and −10V) and stops varying with the dial.	U4 and U1 circuits.
10 Check that, as the dial is rotated from .004 to 4.0, U4 pin 13 varies from approximately +15V to +10V.	U4 and U1 circuits.
11. Check for nonlinearity in the ±1.25V triangle at the emitter of Q11 near the bottom of the ×1K through ×1M ranges.	a. Associated timing capacitors or C36. b. U5, CR6. c. Q9, Q10.
12. Check frequencies of ×1K, ×10K and 100K ranges.	a. Adjust R19. b. C30, 31 and 32 (trimmed by C20).
13. Check frequency and linearity of ×1M range.	a. C34. b. C36 nominal value. c. C18, 19, 20 and 21.
14. Check frequencies of ×1, ×10 and ×100 ranges.	R86 and table 6-9.
15. Select log mode. Check that as the frequency dial is rotated from 4.0 to .0004, U3 pin 4 varies from approximately −.65V to −.45V.	a. U3. b. U1 circuit.

Table 6-8. Symmetry

Indication: Waveform symmetry problem.

Check	If Faulty, Check
1. Set controls to initial positions (refer to paragraph 6.4).	Check for normal operation.
2. If symmetry problem appears on ×1, ×10, ×100 ranges only, problem may be R90 adjustment or go to table 6-9.	
3. Perform steps 5 through 12 of table 6-7, then return to this table.	a. R49 adjustment. b. R45 adjustment.
4. Verify RUN signal at cathode CR6 is approximately +3.5V.	Go to table 6-10.
5. Verify U6 pin 4 and U6 pin 15 vary from approximately −10 to −15V as dial is rotated from 4.0 to .004.	U1, U6, R52, R53.
6. Verify amplitude of SQR signal at cathode CR10 is approximately ±2V.	a. Q7 circuit. b. U7 circuit. c. +5V supply.
7. Check U5, CR6.	

Table 6-9. Capacitance Multiplier

Indication: Problem on bottom frequency ranges only.

Check	Corrective Procedure
1. Set controls to initial positions (refer to paragraph 6.4).	Check for normal operation on ×1K range.
2. Check for 0 Vdc at U11 pins 2 and 6.	SW2 - SW4.
3. Check for approximately 0 Vdc at U11 pin 12.	U11 circuitry.
4. Check for 0 Vdc ±5 mV at U11 pin 10.	a. R90 adjustment. b. U11 circuitry.
5. Select ×100 range; check U11 pin 10 for heavy oscillations.	C46, U11.
6. Check that the signal at U11 pin 2 is amplified by approximately 6 at pin 12 (within saturation limits).	U11 circuitry.
7. Check for the same signal at U11 pins 6 and 7 as at the emitter of Q11.	SW4, U11 circuitry.
8. Ensure that R93 and R94 are shorted in the ×100 range.	SW4.

Table 6-9. Capacitance Multiplier (Continued)

Indication: Problem on bottom frequency ranges only.

Check	Corrective Procedure
9. Check 400 Hz frequency (2.0 × 100).	a. R86 adjustment. b. R89, R95, C45.
10. Check 40 Hz frequency (2.0 × 10).	R93, SW3.
11. Check 4 Hz frequency (4.0 × 1).	R94.
12. Check symmetry at 0.2 × 100; ensure triangle is linear.	a. R90 adjustment. b. U11. c. Leaky C30, C36, C45, C46, CR6, U5, Q9.

Table 6-10. Trigger Logic

Indication: Generator trigger and gate mode problems.

Check	If Faulty, Check
1. Set controls to initial positions (refer to paragraph 6.4).	Check for normal continuous operation.
2. If generator operates normally in continuous mode, go to step 7.	
3. Check for 0V at U9 pins 2 and 5.	SW9.
4. Check for a TTL low at U10 pin 10.	U9, +5V supply.
5. Check for +5V at U10 pin 9.	a. U10. b. CR6, CR15, Q8. c. U6.
6. Check for approximately +3.5V at anode CR6. Check for normal continuous mode operation.	a. CR6, U6, Q8. b. Go to table 6-2.
7. Check that U6 pin 4 and U6 pin 15 vary from approximately −10V to −15V as dial is rotated from 4.0 to .004.	a. U6, R52, R53. b. Go to table 6-7.
8. Go to gated mode (CONT depressed, TRIG/GATE released). Check U9 pin 2 for a TTL high.	a. U10. b. SW9, SW11, +5V supply.

Table 6-10. Trigger Logic (Continued)

Indication: *Generator trigger and gate mode problems.*

Check	Corrective Procedure
9. Check U9 pin 1 for a TTL high.	a. U9. b. R73, −5V supply.
10. Check U10 pin 10 for a TTL high.	a. U9. b. U10.
11. Check U10 pin 9 for TTL low.	a. U10. b. Q8.
12. Check anode CR6 for approximately −1.5V.	a. CR15, Q8, R78. b. CR6.
13. Check cathode CR6 for approximately −0.7V.	a. U5. b. U6.
14. Check emitter Q11 for 0 Vdc ±100 mV.	a. R81 adjustment. b. Q9 - Q11 circuitry.
15. Connect an external TTL signal to TRIG IN connector; check for the inverse of that signal at U10 pin 10.	a. E25, E26. b. CR13, CR14. c. U9, SW10.
16. Press TRIG/GATE switch and check for an approximate 20 ns negative pulse at U10 pin 10 following the low-to-high transition of the external signal (increasing the frequency of the external generator makes this pulse more visible).	a. U9, SW10. b. C29.
17. Remove the external signal and verify that U10 pin 5 goes from high to low when the MAN TRIG switch is held depressed.	a. SW11. b. U10.
18. Release the TRIG/GATE switch (gated) and check that U10 pin 10 goes from high to low when the MAN TRIG switch is pressed.	SW9.
19. Monitor 50Ω OUT, triangle function, for 0 Vdc baseline.	R81, R112 adjustments.
20. Press MAN TRIG switch and check 50Ω OUT for a continuous triangle while the switch is held. Depress TRIG/GATE switch (triggered) and verify a single cycle output each time the MAN TRIG switch is depressed.	a. U10 or clock signal to U10 from U7. b. C29 (pulse too narrow).

Table 6-11. Sweep Circuit

Indication: Sweep or Ramp problems.

Check	Corrective Procedure
1. Depress CONT (Sweep). Extend SWP/STOP. Check E48 for +4V.	a. U1. b. SW2.
2. Depress SWP/STOP. Check collector of Q3. 4V peak ramp. If ramp amplitude is >4V peak. If ramp amplitude is <4V peak.	 U1, Q3. Q1, Q2.
4. At SWP OUT check for period change of approximately 30 ms to 60s as the TIME control is rotated from full ccw to full cw.	Q2, R22.
5. At SWP OUT check for ramp period drift.	C4.
6. At E39 with STOP control full cw, ramp amplitude is 4V peak, with STOP control full ccw, ramp amplitude is 0V.	R12, SW1, SW2.

SECTION 7
PARTS AND SCHEMATICS

7.1 DRAWINGS

The following assembly drawings (with parts lists) and schematics are in the arrangement shown below.

7.2 ORDERING PARTS

When ordering spare parts, please specify part number, circuit reference, board, serial number of unit, and, if applicable, the function performed.

7.3 ADDENDA

Under Wavetek's product improvement program, the latest electronic designs and circuits are incorporated into each Wavetek instrument as quickly as development and testing permit. Because of the time needed to compose and print instruction manuals, it is not always possible to include the most recent changes in the initial printing. Whenever this occurs, addendum pages are prepared to summarize the changes made and are inserted immediately inside the rear cover. If no such pages exist, the manual is correct as printed.

Drawing	Drawing No.
Instrument Schematic	0004-00-0166
Chassis Assembly	0102-00-0837
Chassis Parts List	1101-00-0837
Generator Board Schematic	0103-00-0835
Generator Board Assembly	1100-00-0835
Generator Board Parts List	1100-00-0835
Sweep Board Schematic	0103-00-0818
Sweep Board Assembly	1100-00-0818
Sweep Board Parts List	1100-00-0818

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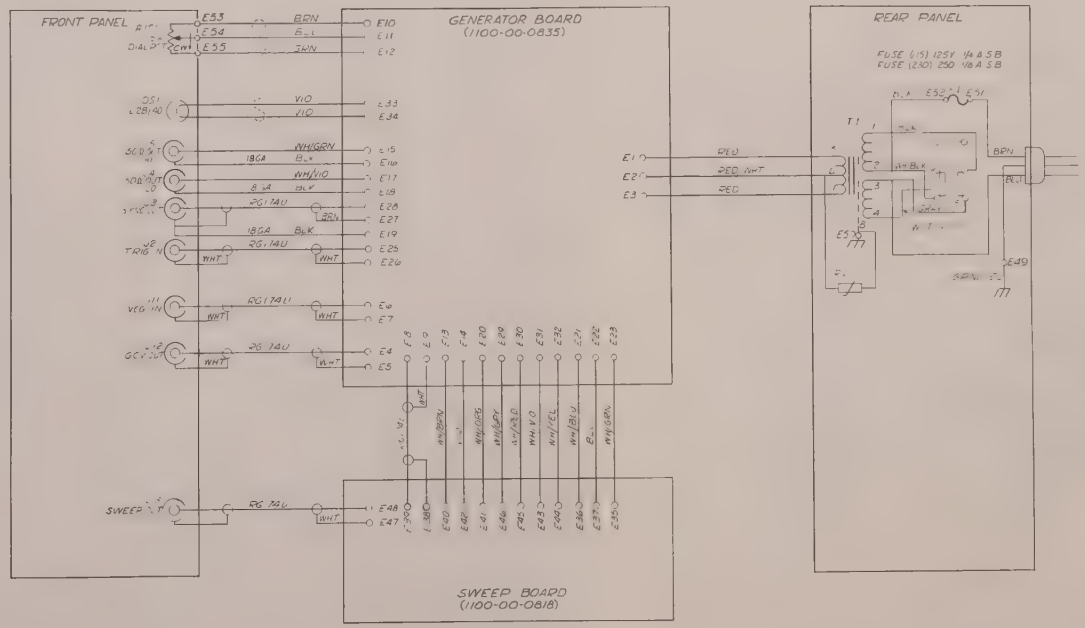
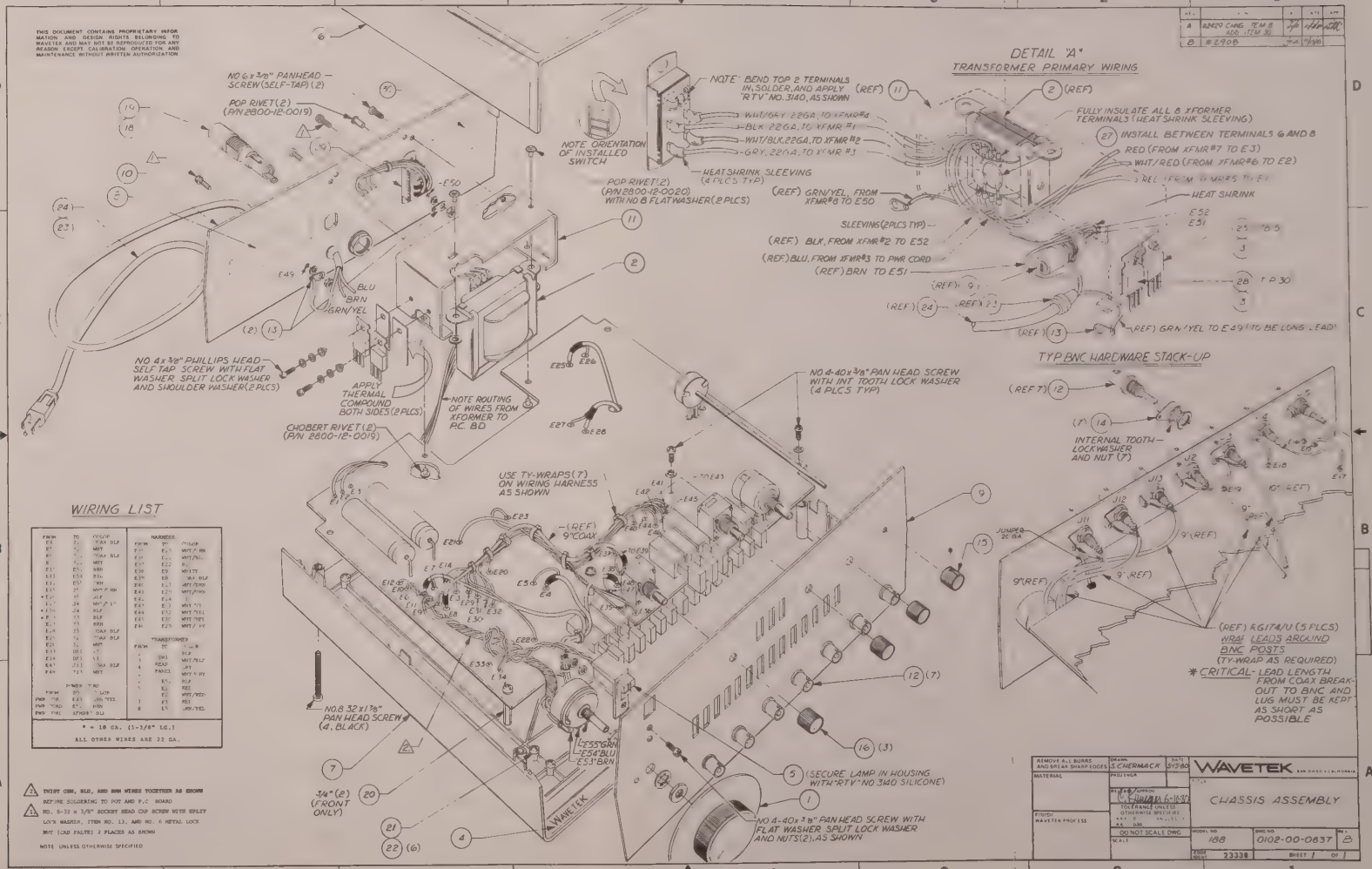


NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN: ED - FEB	DATE: 2-84	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	PROTINGR		SCHEMATIC, INSTRUMENT
FINISH WAVETEK PROCESS	RELEASE APPROV: C.K. Wu 6-18-90		
	TOLERANCE UNLESS OTHERWISE SPECIFIED .XX = .01 ANGLES ± .XX = .005		
	DO NOT SCALE DWG	MODEL NO: 173	DWG NO: 0004-00-0166 REV
		CODE IDENT: 23338	SHEET 1 OF 1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DETAIL "A"
TRANSFORMER PRIMARY WIRING
NOTE: BEND TOP 2 TERMINALS IN, SOLDER, AND APPLY "RTV" NO. 3140, AS SHOWN
WHT/GRY, 22GA, TO XFMR #4
BLK, 22GA, TO XFMR #1
WHT/BLK, 22GA, TO XFMR #2
GRY, 22GA, TO XFMR #3
HEAT SHRINK SLEEVING (4 PLCS TYP.)
NOTE ORIENTATION OF INSTALLED SWITCH
POP RIVET (2) (P/N 2800-12-0020) WITH NO 8 FLAT WASHER (2 PLCS)
(REF) GRN/YEL, FROM XFMR #8 TO E50
FULLY INSULATE ALL 8 XFORMER TERMINALS (HEAT SHRINK SLEEVING)
INSTALL BETWEEN TERMINALS 6 AND 8
RED (FROM XFMR #7 TO E3)
WHT/RED (FROM XFMR #6 TO E2)
BLK (FROM XFMR #5 TO E1)
HEAT SHRINK
E52
E51
SLEEVING (2 PLCS TYP)
(REF) BLK, FROM XFMR #2 TO E52
(REF) BLU, FROM XFMR #3 TO PWR CORD
(REF) BRN TO E51
(REF) GRN/YEL TO E49 (TO BE LONG LEAD)
TP 30
NO 6 x 3/8" PANHEAD SCREW (SELF-TAP) (2)
POP RIVET (2) (P/N 2800-12-0019)
E50
E49
BLU
BRN
GRN/YEL
NO 4 x 3/8" PHILLIPS HEAD SELF TAP SCREW WITH FLAT WASHER SPLIT LOCK WASHER AND SHOULDER WASHER (2 PLCS)
APPLY THERMAL COMPOUND BOTH SIDES (2 PLCS)
NOTE ROUTING OF WIRES FROM XFORMER TO P.C. BD.
E25
E26
E27
E28
CHOBERT RIVET (2) (P/N 2800-12-0019)
NO 4-40 x 3/8" PAN HEAD SCREW WITH INT TOOTH LOCK WASHER (4 PLCS TYP)
TYP BNC HARDWARE STACK-UP
INTERNAL TOOTH LOCKWASHER AND NUT (7)
JUMPER 20 GA
(REF) KG174/U (5 PLCS)
WRAP LEADS AROUND BNC POSTS
(TY-WRAP AS REQUIRED)
* CRITICAL- LEAD LENGTH FROM COAX BREAK-OUT TO BNC AND LUG MUST BE KEPT AS SHORT AS POSSIBLE
USE TY-WRAPS (7) ON WIRING HARNESS AS SHOWN
(REF) 9" COAX
E23
E24
E1
E2
E3
E4
E5
E6
E7
E8
E9
E10
E11
E12
E13
E14
E15
E16
E17
E18
E19
E20
E21
E22
E29
E30
E31
E32
E33
E34
E35
E36
E37
E38
E39
E40
E41
E42
E43
E44
E45
E46
E47
E48
NO 8-32 x 1/2" PAN HEAD SCREW (4, BLACK)
3/4" (2) (FRONT ONLY)
E55 GRN
E54 BLU
E53 BRN
WAVETEK
(SECURE LAMP IN HOUSING WITH "RTV" NO 3140 SILICONE)
NO 4-40 x 3/8" PAN HEAD SCREW WITH FLAT WASHER SPLIT LOCK WASHER AND NUTS (2), AS SHOWN
WIRING LIST
FROM TO COLOR
E1 J1 22GA BLK
E2 J2 WHT
E3 J3 22GA RED
E4 J4 RED
E5 J5 BRN
E6 J6 WHT
E7 J7 BLK
E8 J8 WHT/BRN
E9 J9 ORN
E10 J10 WHT/ORN
E11 J11 GRY
E12 J12 WHT/GRN
E13 J13 WHT/BLU
E14 J14 BLK
E15 J15 22GA BLK
E16 J16 WHT
HARNESS
FROM TO COLOR
E31 E41 WHT/BRN
E32 E42 "
E33 E43 "
E34 E44 WHT/BLU
E35 E45 "
E36 E46 WHT/ORN
E37 E47 "
E38 E48 WHT/GRY
E39 E49 "
E40 E50 WHT/YEL
E41 E51 WHT/BRN
E42 E52 WHT/RED
E43 E53 WHT/GRN
E44 E54 WHT/BLU
E45 E55 WHT/VIO
TRANSFORMER
FROM TO COLOR
1 GRY BLK
2 RED WHT/RED
3 PANEL WHT/GRY
4 " BLK
5 " RED
6 E52 WHT/RED
7 E51 WHT/BRN
8 E50 WHT/YEL
POWER CORD
FROM TO COLOR
PWR "HOT" E49 GRN/YEL
PWR "COLD" E51 BRN
PWR "GND" ATTACH TO BLU
* = 18 GA. (1-3/8" LG.)
ALL OTHER WIRES ARE 22 GA.
TWIST GRN, BLU, AND BRN WIRES TOGETHER AS SHOWN BEFORE SOLDERING TO POT AND P.C. BOARD
NO. 6-32 x 3/8" SOCKET HEAD CAP SCREW WITH SPLIT LOCK WASHER, ITEM NO. 13, AND NO. 6 METAL LOCK NUT (CAD PLATE) 2 PLACES AS SHOWN
NOTE UNLESS OTHERWISE SPECIFIED
REMOVE ALL BURRS AND BREAK SHARP EDGES
MATERIAL
FINISH
WAVETEK PROPRIETARY
DRAWN S. CHERMACK 5/2/80
PROJ MGR
APPROVED
TOLERANCE UNLESS OTHERWISE SPECIFIED
DO NOT SCALE DWG
SCALE
WAVETEK SAN DIEGO, CALIFORNIA
CHASSIS ASSEMBLY
MODEL NO. 188
DRAWING NO. 0102-00-0837
REV. B
CODE IDENT 23338
SHEET 1 OF 1

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.
NONE	ASSY DRWG CHASSIS	0102-00-0837		
		180-0029		
		182A-0041		
		120A-00-1013		
	BOTTOM COVER	120A-00-1014		
	INSULATOR MICA	142-211		
	INDICATOR DIAL	180-303		
	LABEL WARNING			
		1400-00-9473		
		1400-01-1440		
	REAR PANEL	1400-01-1843		
	LABEL RATING	1400-01-4610		
	BNC JOHN	MC-7946		
	SOLDER LUG	1497		
	SOLDER LUG	1405-6		
	KNOB SMALL			
	KNOB 1 3/4" DIA/DIAL	313.250		

WAVETEK PARTS LIST

TITLE

ASSEMBLY NO 1101-00-0837 REV

PAGE 1

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART	MFGR	WAVETEK NO.
	FUSE HOLD	031.1652/031.1666		
	STANDOFF MALE FEMALE .750 H..250 HEX 4-40	1443-M03-F05-440		
	STRAIN RELIEF BUSH	SR6W		
	VARISTOR	V56ZA8		
	TRANS	TIP-30		
	SWITCH ASSY SLIDE	4a25a-LF		
	SOLDER GUARD	4a25a-LF-SG		
	PWR CORD	0-7789-008-0Y	PACEC	4301-80-0004
	VOLTAGE REGULATOR	MC7815	MOT	7000-78-1500

WAVETEK PARTS LIST

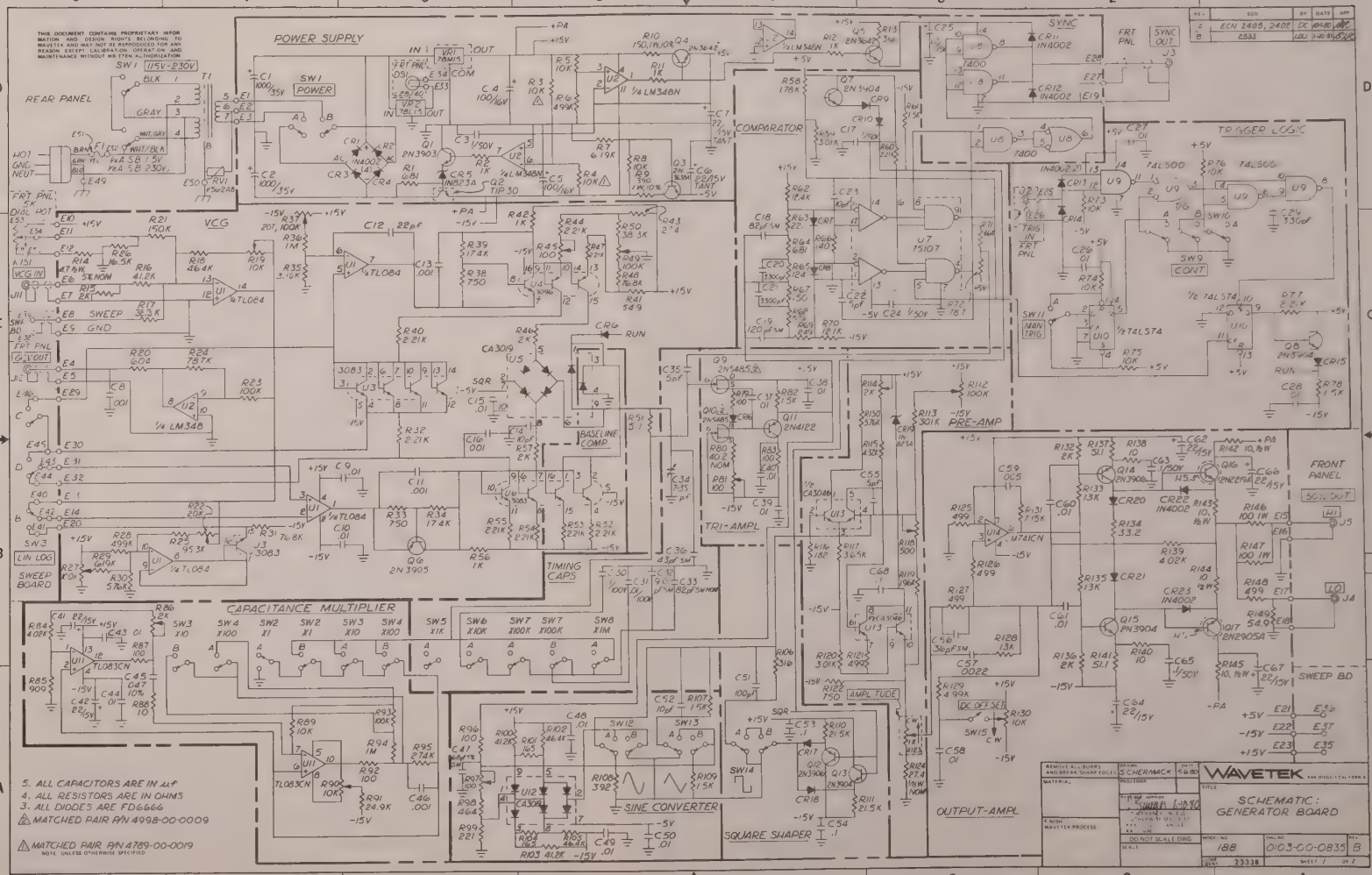
TITLE

ASSEMBLY NO 1101-00-0837 REV

PAGE 2

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	PROJ/ENGR		TITLE PARTS LIST CHASSIS
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED .XXX ± .010 ANGLES ± 1° .XX ± .030		
	DO NOT SCALE DWG	MODEL NO 188	DWG NO 1101-00-0837 REV C
	SCALE	CODE IDENT NO 23338	SHEET 1 OF 1

NOTE UNLESS OTHERWISE SPECIFIED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POWER SUPPLY
REAR PANEL
SW1 115V-230V
SW1 POWER
VCG
SWEEP
GND
VCG IN
LIN LOG
SWEEP BOARD
FRT PNL
SYNC
SYNC OUT
COMPARATOR
TRIGGER LOGIC
TRIS IN
MAN TRIG
SW9 CONT
RUN
PRE-AMP
TRI-AMPL
BASELINE COMP
TIMING CAPS
CAPACITANCE MULTIPLIER
SW3 X10
SW4 X100
SW2 X1
SW2 X10
SW3 X100
SW4 X100
SW5 X1K
SW6 X10K
SW7 X100K
SW7 X1M
SW8 X1M
SINE CONVERTER
SQUARE SHAPER
AMPL TUDE
DC OFFSET
OUTPUT-AMPL
FRONT PANEL
SWEEP BD
ECN 2405, 2406
2533
1. ALL CAPACITORS ARE IN μf
4. ALL RESISTORS ARE IN OHMS
3. ALL DIODES ARE FD6666
MATCHED PAIR P/N 4998-00-0009
MATCHED PAIR P/N 4789-00-0019
NOTE UNLESS OTHERWISE SPECIFIED
WAVETEK
SCHEMATIC: GENERATOR BOARD
188
0103-00-0835
B
23338

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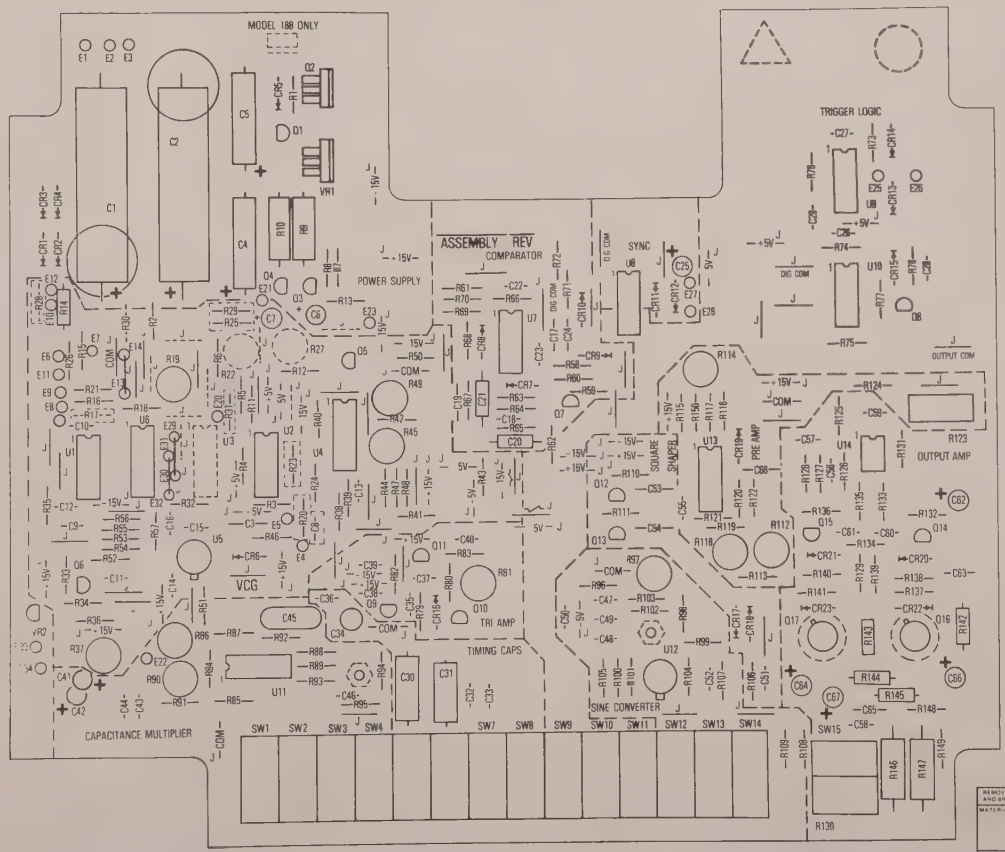

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	ENGINEER		TITLE: GENERATOR BOARD ASSEMBLY
FINISH WAVETEK PROCESS	MFG ENGR APPROV		
	TOLERANCE UNLESS OTHERWISE SPECIFIED .XXX .010 ANGLES ± .XX .030		
	DO NOT SCALE DWG		MODEL NO 188 DRAWING 1100-00-0835 REV
	SCALE		SIZE A2 DWG NO 23338 SHEET 1 OF 1

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	ASSY DRWG,GEN BD	0101-00-0835	WAVE	0101-00-0835	1
NONE	SCHEMATIC, GEN BD	0103-00-0835	WAVE	0103-00-0835	1
[illegible]	CAPACITOR 5PF 1KV	DD-050	[illegible]	1500-00-5011	[illegible]
[illegible]	CAPACITOR 10PF 1KV	DD-10C	[illegible]	1500-01-0011	2
[illegible]	[illegible] 100PF 1KV	DD-101	CRL	1500-01-0111	1
[illegible]	[illegible] .01KV 1KV	DD-102	ARCO	1500 01 0211	5
[illegible]	[illegible] MN .01NF 50V	CAC02Z5U103Z100A	CORNG	1500-01-0210	.8
[illegible]	[illegible] MON .1NF 50V	CAC032U104Z050A	[illegible]	1500-01-0405	8
[illegible]	[illegible] 22PF 1KV	DD-220	[illegible]	1500-02-a011	[illegible]
[illegible]	[illegible]	DD-222 LONG LEAD	[illegible]	1500 02-2201	[illegible]
[illegible]	[illegible] 33LPF 1KV	DD-331	ARCO	1500-03-3111	[illegible]
[illegible]	[illegible] C MN 3300PF 50V	1801A1R030A332J	VROYN	1500-03-3205	[illegible]
[illegible]	CAP CER .005MF 50V	CK-532	[illegible]	1500-05-0210	[illegible]
[illegible]	[illegible] MICA 130PF 500V	DM15-101J	[illegible]	1500-11-0100	[illegible]
[illegible]	CAP MICA 120PF 500V	DM15-121J	[illegible]	1500 11-2100	[illegible]
[illegible]	CAP MICA 36PF 500V	DM15 360J	[illegible]	1500-12-6000	[illegible]

WAVETEK PARTS LIST — TITLE: PCA GENERATOR BD — ASSEMBLY NO: 1100-00-0835 — PAGE 1 — REV D

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
[illegible]	TRANSIPAD	10160	METRS	2800-11-0004	2
R45 R81 R87	POT,TRIM 100	91AR100	BECK	4600-01-0103	3
R19 R90	POT TRIM 10K	91AR10K	BECK	4600-01-0319	2
R112 R27 R49	POT TRIM 100K	91AR100K	BECK	4600-01-0402	3
R114 R86	POT TRIM 2K	91AR2K	BECK	4600-02-0201	2
R22	POT TRIM 20K	91AR20K	BECK	4600-02-0301	1
R118	POT TRIM 500	91AR50D	BECK	4600-05-0104	1
R130	POT SWITCH 10K	GH-1879	CTS	4602-01-0300	1
R123	POT CONT 1K FROM 4600-01-0207	4609-71-0201	MFTH	4609-71-0201	1
R37	POT TRIM 20T 100K	68WR100K	BECK	4609-90-0001	1
R142 R143 R144 R145	RES,C 1/2W,5% 10	RC20GF-100	STKPL	4700 25-0100	4
R [illegible]	RES C 1/2W,5% 4.7	RC20GF-4R7	STKPL	4700-25-0479	1
[illegible]	RES C 1W 10% 150	RC32GF151J	AB	4700-36-1500	1
R9	RES C 1W 10% 390	RC32GF391J	AB	4700-36-3900	1
R78 R83 R87 R92 R96	RES MF 1/8W,1% 100	RN55D-1000F	TRW	4701-03-1000	5
R11 R12 R2 R42 R56	RES MF 1/8W,1% 1K	RN55D-1001F	TRW	4701-03-1001	5
R5 R73 R74 R75 R76 R8	RES MF 1/8W,1% 10K	RN55D-1002F	TRW	4701-03-1002	[illegible]

WAVETEK PARTS LIST — TITLE: PCA GENERATOR BD — ASSEMBLY NO: 1100-00-0835 — PAGE 3 — REV D

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R110 R111	RES MF 1/8W,1% 21.5K	RN55D-2152F	TRW	4701-03-2152	2
R124T	RES MF 1/8W,1% 21.5	RN55D 2183F	TRW	4701-03-2159	[illegible]
R63 R99	RES MF 1/8W,1% 221	RN55D-2210F	TRW	4701-03-2210	[illegible]
R32 R40 R44 R47 R52 R53 R54 R55 R60 R77	RES MF 1/8W,1% 2.21K	RN55D-2211F	TRW	4701-03-2211	[illegible]
R89	RES MF,1 8W,1% 249	RN55D 2490F	TRW	4701-03-2490	[illegible]
R91	RES MF,1/8W,1% 24.9K	RN55D-2492F	TRW	4701-03-2492	[illegible]
R95	RES MF 1/8W,1% 2.74K	RN55D-2741F	TRW	4701-03-2741	[illegible]
R43	RES MF 1 8W,1% 27.4	RN55D-27R4F	TRW	4701-03-2749	[illegible]
R120 R59	RES MF 1/8W,1% 3.01K	RN55D-3011F	TRW	4701-03 3011	[illegible]
R113	RES MF 1/8W 1% 301K	RN55D-3013F	TRW	4701-03-3013	[illegible]
R106 R3	RES MF 1/8W,1% 31K	RN55D-3162F	TRW	4701-03-3160	[illegible]
R35	RES MF 1/8W 1% 3.16K	RN55D-3161F	TRW	4701-03-3161	[illegible]
R134	RES MF 1/8W,1% 33.2	RN55D-33R2F	TRW	4701 03-3329	[illegible]
R117	RES MF 1/8W,1% 3.65K	RN55D-3651F	TRW	4701-03-3651	[illegible]
R17 R30	RES MF 1/8W,1% 38.3K	RN55D-3832F	TRW	4701-03-3812	[illegible]
R100	RES MF 1/8W,1% 392	RN55D-3920F	TRW	4701-03-3920	[illegible]
R139 R84	RES MF 1 8W,1% 4.02K	RN55D 4021F	TRW	4701-03-4021	[illegible]

WAVETEK PARTS LIST — TITLE: PCA GENERATOR BD — ASSEMBLY NO: 1100-00-0835 — PAGE 5 — REV [illegible]

[illegible]	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
[illegible]	[illegible] MICA 43PF [illegible]	DM15-430J	ARCO	1500 14-3000	1
[illegible]	[illegible]	DM13-e80J	ARCO	1500-16-8000	1
[illegible]	[illegible] 82PF	DM13-820J	ARCO	1500-18-2000	1
[illegible]	[illegible] 910PF 100V	DM15-911F	ARCO	1500-19-1101	1
[illegible]	[illegible] ELECT 100MF 15V	5100157Z16D0T	SPRA.	1500-31-0101	2
[illegible]	[illegible] ELECT 1000MF 35V	39D13B003S0L6	SPRA.	1500-31-0212	2
[illegible]	[illegible] POLYC .01MF 100V	PA2B103F	[illegible]	1500-41-0304	1
[illegible]	[illegible] POLYC .1NF 100V	PA2B104F	[illegible]	1500-41-0404	1
[illegible]	[illegible] MYLAR .047MF100V	225P47391WD3	SPRA.	1500-44-7Q14	1
[illegible]	CAPACITOR 35PF 250V	76-TRIKO-02 7/35 PF	TRIM	1500-53-5000	1
[illegible]	CAP TANT 22MF 15V	196D226X9015MA1	SPRA.	1500 72 2601	9
[illegible]	GENERATOR BD	1700 00-0017	WAVE	1700-00-0017	1
[illegible]	LAMP	L28/40	MARK	2400-02-0017	1
NONE	STANDOFF SMALL .875 H. [illegible] 4-40 [illegible]	8926-7/8-2C	[illegible]	2800-05-0003	2
[illegible]	HEAT [illegible]	[illegible] 237	WAKE	2800-11-0001	2

WAVETEK PARTS LIST — TITLE: PCA GENERATOR BD — ASSEMBLY NO: 1100-00-0835 — PAGE 2 — REV D

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R69	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
R23 R93	RES,MF 1/8W,1% 100K	RN55D-1003F	TRW	4701-03-1003	8
R138 R140 R88	RES,MF 1/8W,1% 12	RN55D 1R0F	TRW	4701-03-1209	3
R72	RES MF 1 8W 1% 12.1K	RN55D-1212F	TRW	4701-03-1212	1
R65	RES MF,1/8W,1% 124	RN55D-1240F	TRW	4701-03-1240	1
R94	RES MF,1/8W,1% 12.4K	RN55D-1242F	TRW	4701-03-1242	1
R128 R133 R135	RES MF,1/8W,1% 13K	RN55D-1302F	TRW	4701-03-1302	3
R66	RES,MF 1/8W,1% 140	RN55D-1400F	TRW	4701-03-1400	1
R67	RES MF,1/8W,1% 150	RN55D-1500F	TRW	4701-03-1500	1
R107 R109 R61 R70 R82	RES MF,1/8W,1% 1.5K	RN55D-1501F	TRW	4701-03-1501	5
R21	RES MF 1/8W,1% 150K	RN55D 1503F	TRW	4701-03-1503	1
R101 R104	RES MF 1/8W,1% 165	RN55D-1650F	TRW	4701-03-1650	2
R26	RES,MF 1/8W,1% 16.5K	RN55D-1652F	TRW	4701-03-1652	1
R34 R39	RES,MF,1/8W,1% 17.4K	RN55D-1742F	TRW	4701-03-1742	2
R58	RES MF,1/8W,1% 1.78K	RN55D-1781F	TRW	4701-03-1781	1
R116	RES MF 1/8W,1% 182	RN55D-1820F	TRW	4701-03-1820	1
R119	RES MF,1/8W,1% 1.96K	RN55D-1961F	TRW	4701-03-1961	1
R132 R136 R15 R46 R5	RES MF,1/8W,1% 2K	RN55D-2001F	TRW	4701-03-2001	5

WAVETEK PARTS LIST — TITLE: PCA GENERATOR BD — ASSEMBLY NO: 1100-00-0835 — PAGE 4 — REV B

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R85	RES MF 1/8W,1% 40.2	RN55D 40R2F	TRW	4701-03-4029	1
R100 R103 R16	RES MF,1/8W,1% 41.2K	RN55D-4122F	TRW	4701-03-4122	3
R115	RES MF 1/8W,1% 4.32K	RN55D-4321F	TRW	4701-03-4321	[illegible]
R71 R90	RES MF 1/8W,1% 464	RN55D-4640F	TRW	4701-03 4640	[illegible]
R102 R105 R18	RES MF,1/8W,1% 46.4K	RN55D-4642F	TRW	4701-03-4642	3
R121 R125 R126 R127 R148	RES MF 1/8,1% 499	RN55D-4990F	TRW	4701-03-4990	5
R129 R6	RES MF,1/8W,1% 4.99K	RN55D-4991F	TRW	4701-03-4991	[illegible]
R137 R141 R51	RES MF,1/8W,1% 51.1	RN55D 51R1F	TRW	4701-03-5110	3
R149 R41	RES MF 1/8W,1% 54.9	RN55D-54R9F	TRW	4701-03-5499	2
R68	RES MF 1 8W 1% 57.6	RN55D-57R6F	TRW	4701-03 5760	1
R140 R31	RES MF 1 8W,1% 5.76K	RN55D-5761F	TRW	4701-03-5761	[illegible]
R20	RES MF 1 8W,1% 604	RN55D-6040F	TRW	4701-03-6040	1
R7	RES MF 1 8W,1% 6.19K	RN55D-6191F	TRW	4701-03-6191	1
R1 R64	RES MF 1/8W,1% 681	RN55D-6810F	TRW	4701-03-6810	[illegible]
R131	RES MF,1/8W,1% 7.15K	RN55D-7151F	TRW	4701-03-7151	1
R122 R33 R38	RES MF 1/8W,1% 750	RN55D-7500F	TRW	4701-03-7500	3
R31 R48	RES MF 1/8W,1% 76.8K	RN55D-7682F	TRW	4701-03-7682	2

WAVETEK PARTS LIST — TITLE: PCA GENERATOR BD — ASSEMBLY NO: 1100-00-0835 — PAGE 6 — REV D

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES — MATERIAL — FINISH WAVETEK PROCESS — DRAWN — PROJ ENGR — RELEASE APPROV — DATE — TOLERANCE UNLESS OTHERWISE SPECIFIED XXX ± .010 ANGLES 1 XX ± .02 — DO NOT SCALE DWG — SCALE

WAVETEK SAN DIEGO, CALIFORNIA

TITLE: PARTS LIST GENERATOR BD

MODEL NO 188 — DWG NO 1100-00-0835 — REV D

FSCM NO 27338 — SHEET 1 OF 8

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
[illegible]	[illegible]	RN55D-7872F	TRW	4701-03-7872	1
[illegible]	[illegible]	RN55D-7879F	TRW	4701-03-7879	1
[illegible]	[illegible]	RN55D-9090F	TRW	4701-03-9090	1
[illegible]	[illegible]	RN55D 9532F	TRW	4701-03-9532	1
[illegible]	[illegible]	RN60D-1004F	TRW	4701-13-1004	4
R20	[illegible] 499K	RN60D-4993F	TRW	4701-13-4993	1
R22	[illegible]	RN60D-6193F	TRW	4701-13-6193	3
R[illegible]	[illegible]	RN70D-1000F	TRW	4701-33-1000	4
R3 R4	RES SET 2-10K 1-8W STY 2 4701-03-1002	142-501-64A	WVTK	4789-00-0019	2
[illegible]	DIODE ZENER 6.2V	1N823A	NPC	4801-01-0823	2
[illegible]	DIODE	SCE-1	SEMT	4801-02-2001	1
[illegible]	[illegible]	42 blue	[illegible]	4807 02 6888	3
[illegible]	TRANS	2N2219A	NSC	4901-02-2191	1
[illegible]	TRANS	2N2907A	NSC	4901-02-9031	1
[illegible]	TRANS	2N3638A	CAPTR	4901-03-6381	1

WAVETEK PARTS LIST | TITLE PCA. GENERATOR BD | ASSEMBLY NO. 1100-00-0835 PAGE 7 | REV D

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
U7	IC	SN75107AN	[illegible]	[illegible]	[illegible]
U8	IC	7400	[illegible]	[illegible]	[illegible]
U9	IC	74LS00	[illegible]	[illegible]	[illegible]
U10	IC	74LS74	TI	[illegible]	[illegible]

WAVETEK PARTS LIST | TITLE PCA. GENERATOR BD | ASSEMBLY NO. 1100-00-0835 PAGE 9 | REV [illegible]

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
J4 Q5	TRANS	2N3642	FAIR	4901-03-6420	2
Q1	TRANS	2N3903	NSC	4901-03-9030	1
Q13 Q15 Q7 Q8	TRANS	2N3904	FAIR	4901-03-9040	4
[illegible]	TRANS	2N3905	ITT	4901-03-9050	1
[illegible]	TRANS	2N3906	FAIR	4901-03-9060	2
[illegible]	TRANS	2N4122	NSC	4901-04-1220	1
[illegible]	TRANS M PR 2N5485 QTY 2 4901-05-4850	142-501-53	WVTK	4998-00-0009	1
[illegible]	SWITCH ASSY PB	5103-00-0026	WVTK	5103-00-0026	1
[illegible]	[illegible]	TL083CN	TI	7000-00-8300	1
[illegible]	[illegible]	TL084CN	TI	7000-00-8400	1
U2	[illegible]	LM348N	NSC	7000-03-4800	1
U4	[illegible]	MA-741	FAIR	7000-07-4100	1
[illegible]	[illegible]	CA-3019	FAIR	7000-33-1900	2
[illegible]	[illegible]	CA-3046	RCA	7000-30-4600	1
[illegible]	IC	CA3083	FAIR	7000-30-8300	2
U6	IC	CA-3096AE	RCA	7000-30-9600	1
VR2	IC	78L15	TI	7000-78-1501	1

WAVETEK PARTS LIST | TITLE PCA. GENERATOR BD | ASSEMBLY NO. 1100-00-0835 PAGE 8 | REV D

NOTE: UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	ENGINEER		TITLE: PARTS LIST GENERATOR BD
FINISH: WAVETEK PROCESS	RELEASE APPROV.		
	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX ±.010 ANGLES 1° XX ±.02		MODEL NO. 188
	DO NOT SCALE DWG		DWG NO. 1100-00-0835 REV D
	SCALE		CODE IDENT. 23338 SHEET 2 OF 2

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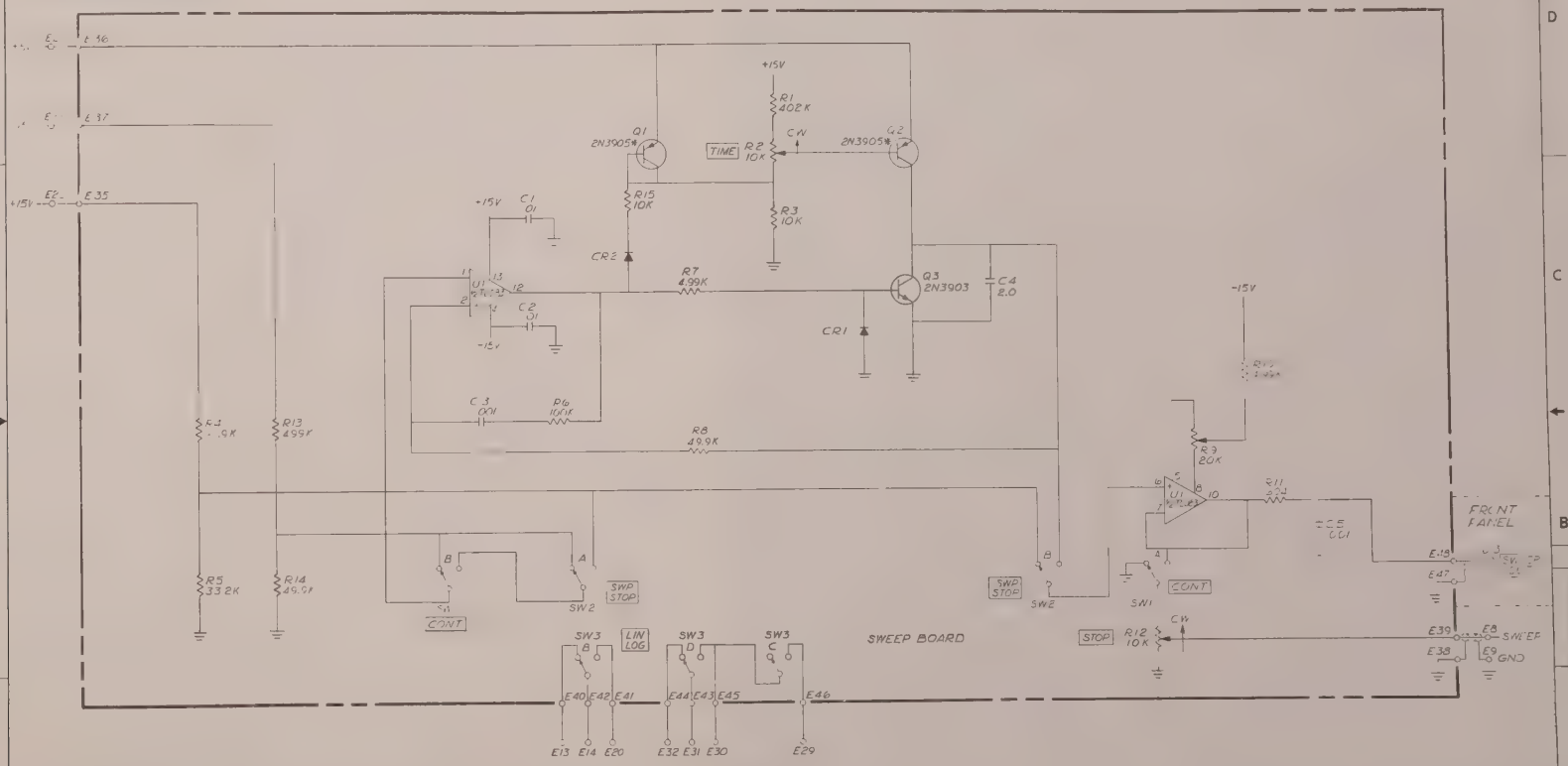

6 SWITCHES SHOWN IN "CONT," "STOP," AND "LIN" MODE
5 * = MATCHED PAIR P/N 4998-00-0049
4 LAST REF DESG USED
R15, C5, CR2, Q3, SW3, E46
3 ALL CAPACITORS ARE IN μF
2 ALL RESISTORS ARE IN OHMS
1 ALL DIODES ARE FDG666
NOTE: UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES
MATERIAL
DRAWN S. CHERMACK 4-29-85
PROJENGR
APPROVED
DO NOT SCALE DWG
SCALE
WAVETEK SAN DIEGO, CALIFORNIA
TITLE: SCHEMATIC: SWEEP BOARD
MODEL NO 188
DWG NO 010700-0918
REV
CODE IDENT 23338
SHEET 1 OF 1

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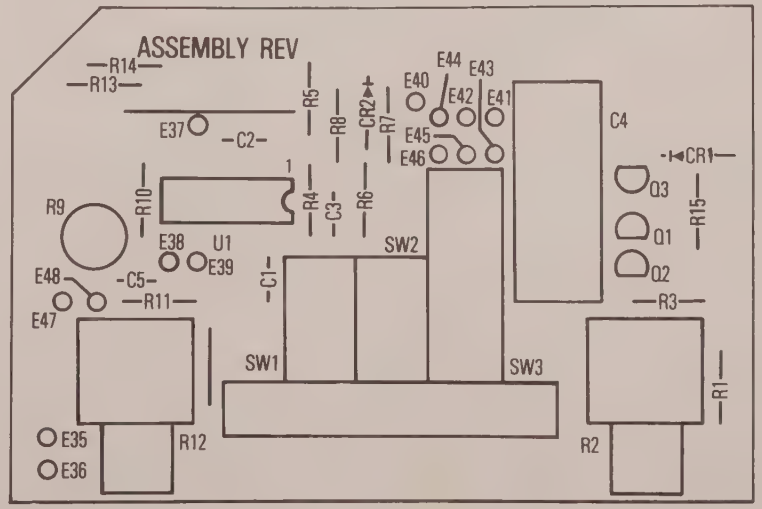

WAVETEK

SWEEP BOARD ASSEMBLY

DO NOT SCALE DWG

DWG NO 1100-00-6818

MODEL NO 188

SHEET 1 OF 1

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	DRIO-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	ASSY DRWG SWEEP BD	0101-00-0818	WVTK	0101-00-0818	1
NONE	SCHEMATIC SWEEP BD	0103-00-0818	WVTK	0103-00-0818	1
C2 C5	CAP,CER,.001MF,1KV	DD-102	ARCO	1500-01-0211	2
C1 C2	CAP,CER,5%,.01MF,50V	CAC0225U1032100A	CORNG	1500-01-0310	2
C4	CAP,MYLR 2MF,200V	2MFW205K	AMRAD	1500-42-0504	1
NONE	SWEEP BD	1700-00-0818	WVTK	1700-00-0818	1
R12 R2	POT,CONT,10K	72XM056S103U	AB	4400-01-0322	2
R9	POT,TRIM,20K	91AR20K	BECK	4400-02-0201	1
R15 R3	RES,MF,1/8W,1%,10K	RN55D-1002F	TRW	4701-03-1002	2
R6	RES,MF,1/8W,1%,100K	RN55D-1003F	TRW	4701-03-1003	1
R5	RES,MF,1/8W,1%,33.2K	RN55D-3322F	TRW	4701-03-3322	1
R10 R7	RES,MF,1/8W,1%,4.99K	RN55D-4991F	TRW	4701-03-4991	2
R14 R8	RES,MF,1/8W,1%,49.9K	RN55D-4992F	TRW	4701-03-4992	2
R11	RES,MF,1/8W,1%,604	RN55D-6040F	TRW	4701-03-6040	1
R4	RES,MF,1/8W,1%,90.9K	RN55D-9092F	TRW	4701-03-9092	1
R1	RES,MF,1/4W,1%,402K	RN60D-4023F	TRW	4701-13-4023	1
R13	RES,MF,1/4W,1%,499K	RN60D-4993F	TRW	4701-13-4993	1
CR1 CR2	DIODE	FD-6666	FAIR	4807-02-6666	2

WAVETEK PARTS LIST	TITLE PCA SWEEP BD	ASSEMBLY NO 1100-00-0818 PAGE 1	REV B

REFERENCE DESIGNATORS	PART DESCRIPTION	DRIO-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
Q3	TRANS	2N3903	NSC	4901-03-9030	1
Q1 Q2	TRANS,M/SR 2N3905 QTY 2 4901-03-9050	4998-00-0049	WVTK	4998-00-0049	1
NONE	SWITCH ASSY PB	5103-00-0030	WVTK	5103-00-0030	1
U1	IC	TL083CN	TI	7000-00-8300	1

WAVETEK PARTS LIST	TITLE PCA SWEEP BD	ASSEMBLY NO 1100-00-0818 PAGE 2	REV B

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO CALIFORNIA
MATERIAL	ENG/ENGR		TITLE PARTS LIST SWEEP BD
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED .XXX ± .010 ANGLES ± 1 .X ± .030		
	DO NOT SCALE DWG		MODEL NO 188 DWG NO 1100-00-0818 REV B
	SCALE		CODE IDENT 23338 SHEET 1 OF 1

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